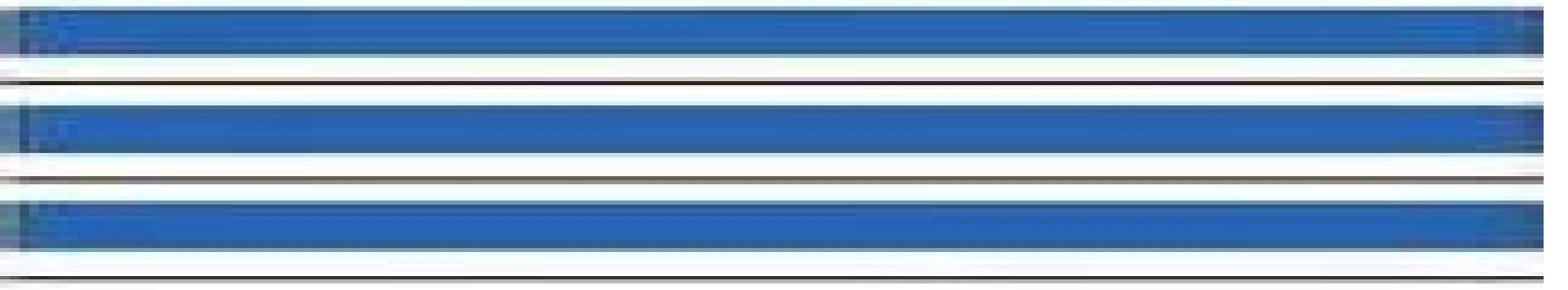


Automatic Programming Applied to VLSI CAD Software: A Case Study

**Dorothy E. Setliff
Rob A. Rutenbar**



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Rutenbar, 1990-05-31 This book and the research it describes resulted from a simple observation we made sometime in 1986 Put simply we noticed that many VLSI design tools looked alike That is at least at the overall software architecture level the algorithms and data structures required to solve problem X looked much like those required to solve problem X Unfortunately this resemblance is often of little help in actually writing the software for problem X given the software for problem X In the VLSI CAD world technology changes rapidly enough that design software must continually strive to keep up And of course VLSI design software and engineering design software in general is often exquisitely sensitive to some aspects of the domain technology in which it operates Modest changes in functionality have an unfortunate tendency to require substantial and time consuming internal software modifications Now observing that large engineering software systems are technology dependent is not particularly clever However we believe that our approach to xiv Preface dealing with this problem took an interesting new direction We chose to investigate the extent to which automatic programming ideas could be used to synthesize such software systems from high level specifications This book is one of the results of that effort

Automatic Programming Applied to VLSI CAD Software Dorothy E Setliff, Rob A Rutenbar, 1990-05-01 *Automatic Programming Applied to VLSI CAD Software: A Case Study* Dorothy E. Setliff, Rob A. Rutenbar, 2012-12-06 This book and the research it describes resulted from a simple observation we made sometime in 1986 Put simply we noticed that many VLSI design tools looked alike That is at least at the overall software architecture level the algorithms and data structures required to solve problem X looked much like those required to solve problem X Unfortunately this resemblance is often of little help in actually writing the software for problem X given the software for problem X In the VLSI CAD world technology changes rapidly enough that design software must continually strive to keep up And of course VLSI design software and engineering design software in general is often exquisitely sensitive to some aspects of the domain technology in which it operates Modest changes in functionality have an unfortunate tendency to require substantial and time consuming internal software modifications Now observing that large engineering software systems are technology dependent is not particularly clever However we believe that our approach to xiv Preface dealing with this problem took an interesting new direction We chose to investigate the extent to which automatic programming ideas could be used to synthesize such software systems from high level specifications This book is one of the results of that effort

Symbolic Analysis for Automated Design of Analog Integrated Circuits Georges Gielen, Willy M.C. Sansen, 2012-12-06 It is a great honor to provide a few words of introduction for Dr Georges Gielen's and Prof Willy Sansen's book Symbolic analysis for automated design of analog integrated circuits The symbolic analysis method presented in this book represents a significant step forward in the area of analog circuit design As demonstrated in this book symbolic analysis opens up new possibilities for the development of

computer aided design CAD tools that can analyze an analog circuit topology and automatically size the components for a given set of specifications Symbolic analysis even has the potential to improve the training of young analog circuit designers and to guide more experienced designers through second order phenomena such as distortion This book can also serve as an excellent reference for researchers in the analog circuit design area and creators of CAD tools as it provides a comprehensive overview and comparison of various approaches for analog circuit design automation and an extensive bibliography The world is essentially analog in nature hence most electronic systems involve both analog and digital circuitry As the number of transistors that can be integrated on a single integrated circuit IC substrate steadily increases over time an ever increasing number of systems will be implemented with one or a few very complex ICs because of their lower production costs

Computational Electronics Karl Hess, J.P. Leburton, U. Ravaioli, 2013-03-14 Large computational resources are of ever increasing importance for the simulation of semiconductor processes devices and integrated circuits The Workshop on Computational Electronics was intended to be a forum for the discussion of the state of the art of device simulation Three major research areas were covered conventional simulations based on the drift diffusion and the hydrodynamic models Monte Carlo methods and other techniques for the solution of the Boltzmann transport equation and computational approaches to quantum transport which are relevant to novel devices based on quantum interference and resonant tunneling phenomena Our goal was to bring together researchers from various disciplines that contribute to the advancement of device simulation These include Computer Science Electrical Engineering Applied Physics and Applied Mathematics The success of this multidisciplinary formula was proven by numerous interactions which took place at the Workshop and during the following three day Short Course on Computational Electronics The format of the course including a number of tutorial lectures and the large attendance of graduate students stimulated many discussions and has proven to us once more the importance of cross fertilization between the different disciplines

Handbook of VLSI Chip Design and Expert Systems A. F. Schwarz, 1993 Offers a conceptual and methodological understanding of chip design and of the fundamental principles in the computer aided design of VLSI circuits and systems CAD/CAS The text covers where why and how expert systems are used in subtasks of CAD/CAS and in the integrated chip design system

Hardware Design and Simulation in VAL/VHDL Larry M. Augustin, David C. Luckham, Benoit A. Gennart, Youm Huh, A. Stanculescu, 2012-12-06 The VHSIC Hardware Description Language VHDL provides a standard machine processable notation for describing hardware VHDL is the result of a collaborative effort between IBM Intermetrics and Texas Instruments sponsored by the Very High Speed Integrated Circuits VHSIC program office of the Department of Defense beginning in 1981 Today it is an IEEE standard 1076 1987 and several simulators and other automated support tools for it are available commercially By providing a standard notation for describing hardware especially in the early stages of the hardware design process VHDL is expected to reduce both the time lag and the cost involved in building new systems and upgrading existing ones VHDL is the result of an

evolutionary approach to language development starting with high level hardware description languages existing in 1981 It has a decidedly programming language flavor resulting both from the orientation of hardware languages of that time and from a major requirement that VHDL use Ada constructs wherever appropriate During the 1980 s there has been an increasing current of research into high level specification languages for systems particularly in the software area and new methods of utilizing specifications in systems development This activity is worldwide and includes for example object oriented design various rigorous development methods mathematical verification and synthesis from high level specifications VAL VHDL Annotation Language is a simple further step in the evolution of hardware description languages in the direction of applying new methods that have developed since VHDL was designed

Subband Image Coding John W. Woods, 2013-06-29 This book concerns a new method of image data compression which will may supplant the well established block transform methods that have been state of the art for the last 15 years Subband image coding or SBC was first performed as such in 1985 and as the results became known at first through conference proceedings and later through journal papers the research community became excited about both the theoretical and practical aspects of this new approach This excitement is continuing today with many major research laboratories and research universities around the world investigating the subband approach to coding of color images high resolution images video including video conferencing and advanced television and the medical application of picture archiving systems Much of the fruits of this work is summarized in the eight chapters of this book which were written by leading practitioners in this field The subband approach to image coding starts by passing the image through a two or three dimensional filter bank The two dimensional 2 D case usually is hierarchical consisting of two stages of four filters each Thus the original image is split into 16 subband images with each one decimated or subsampled by 4x4 resulting in a data conservation The individual channel data is then quantized for digital transmission In an attractive variation an octave like approach herein termed subband pyramid is taken for the decomposition resulting in a total of just eleven subbands

Models for Large Integrated Circuits Patrick DeWilde, Zhen-Qiu Ning, 2012-12-06 A modern microelectronic circuit can be compared to a large construction a large city on a very small area A memory chip a DRAM may have up to 64 million bit locations on a surface of a few square centimeters Each new generation of integrated circuit generations are measured by factors of four in overall complexity requires a substantial increase in density from the current technology added precision a decrease of the size of geometric features and an increase in the total usable surface The microelectronic industry has set the trend Ultra large funds have been invested in the construction of new plants to produce the ultra large scale circuits with utmost precision under the most severe conditions The decrease in feature size to submicrons 0.7 micron is quickly becoming available does not only bring technological problems New design problems arise as well The elements from which microelectronic circuits are built transistors and interconnects have different shape and behave differently than before Phenomena that could be neglected in a four micron

technology such as the non uniformity of the doping profile in a transistor or the mutual capacitance between two wires now play an important role in circuit design This situation does not make the life of the electronic designer easier he has to take many more parasitic effects into account up to the point that his ideal design will not function as originally planned

Gallium Arsenide Digital Circuits Omar Wing,2012-12-06 Gallium Arsenide technology has come of age GaAs integrated circuits are available today as gate arrays with an operating speed in excess of one Gigabits per second Special purpose GaAs circuits are used in optical fiber digital communications systems for the purpose of regeneration multiplexing and switching of the optical signals As advances in fabrication and packaging techniques are made the operating speed will further increase and the cost of production will reach a point where large scale application of GaAs circuits will be economical in these and other systems where speed is paramount This book is written for students and engineers who wish to enter into this new field of electronics for the first time and who wish to embark on a serious study of the subject of GaAs circuit design No prior knowledge of GaAs technology is assumed though some previous experience with MOS circuit design will be helpful A good part of the book is devoted to circuit analysis to the extent that is possible for non linear circuits The circuit model of the GaAs transistor is derived from first principles and analytic formulas useful in predicting the approximate circuit performance are also derived Computer simulation is used throughout the book to show the expected performance and to study the effects of parameter variations

Iterative Identification and Restoration of Images Reginald L. Lagendijk,Jan Biemond,2012-12-06 One of the most intriguing questions in image processing is the problem of recovering the desired or perfect image from a degraded version In many instances one has the feeling that the degradations in the image are such that relevant information is close to being recognizable if only the image could be sharpened just a little This monograph discusses the two essential steps by which this can be achieved namely the topics of image identification and restoration More specifically the goal of image identification is to estimate the properties of the imperfect imaging system blur from the observed degraded image together with some statistical characteristics of the noise and the original uncorrupted image On the basis of these properties the image restoration process computes an estimate of the original image Although there are many textbooks addressing the image identification and restoration problem in a general image processing setting there are hardly any texts which give an indepth treatment of the state of the art in this field This monograph discusses iterative procedures for identifying and restoring images which have been degraded by a linear spatially invariant blur and additive white observation noise As opposed to non iterative methods iterative schemes are able to solve the image restoration problem when formulated as a constrained and spatially variant optimization problem In this way restoration results can be obtained which outperform the less results of conventional restoration filters

Neural Models and Algorithms for Digital Testing S.T. Chadrathar,Vishwani Agrawal,M. Bushnell,1991-06-30 References 82 9

QUADRATIC 0 1 PROGRAMMING 85 9 1 Energy Minimization 86 9 2 Notation and Terminology 87 9 3 Minimization

Technique 88 9 4 An Example 92 9 5 Accelerated Energy Minimization 94 9 5 1 Transitive Closure 94 9 5 2 Additional Pairwise Relationships 96 9 5 3 Path Sensitization 97 9 6 Experimental Results 98 9 7 Summary 100 References 100 10 TRANSITIVE CLOSURE AND TESTING 103 10 1 Background 104 10 2 Transitive Closure Definition 105 10 3 Implication Graphs 106 10 4 A Test Generation Algorithm 107 10 5 Identifying Necessary Assignments 112 10 5 1 Implicit Implication and Justification 113 10 5 2 Transitive Closure Does More Than Implication and Justification 115 10 5 3 Implicit Sensitization of Dominators 116 10 5 4 Redundancy Identification 117 10 6 Summary 119 References 119 11 POLYNOMIAL TIME TESTABILITY 123 11 1 Background 124 11 1 1 Fujiwara's Result 125 11 1 2 Contribution of the Present Work 126 11 2 Notation and Terminology 127 11 3 A Polynomial Time Algorithm 128 11 3 1 Primary Output Fault 129 11 3 2 Arbitrary Single Fault 135 11 3 3 Multiple Faults 137 11 4 Summary 139 References 139 ix 12 SPECIAL CASES OF HARD PROBLEMS 141 12 1 Problem Statement 142 12 2 Logic Simulation 143 12 3 Logic Circuit Modeling 146 12 3 1 Model for a Boolean Gate 147 12 3 2 Circuit Modeling 148 12

Bibliographic Guide to Computer Science, 1990 *Evaluation of Computer Aided Software as a Space Analysis Tool for Outfit Unit Design and Planning* Patrick D. Cahill, 1995 **Neural Networks and Speech Processing** David P. Morgan, Christopher L. Scofield, 1991-02-28 We would like to take this opportunity to thank all of those individuals who helped us assemble this text including the people of Lockheed Sanders and Nestor Inc whose encouragement and support were greatly appreciated In addition we would like to thank the members of the Laboratory for Engineering Man Machine Systems LEMS and the Center for Neural Science at Brown University for their frequent and helpful discussions on a number of topics discussed in this text Although we both attended Brown from 1983 to 1985 and had offices in the same building it is surprising that we did not meet until 1988 We also wish to thank Kluwer Academic Publishers for their professionalism and patience and the reviewers for their constructive criticism Thanks to John McCarthy for performing the final proof and to John Adcock Chip Bachmann Deborah Farrow Nathan Intrator Michael Perrone Ed Real Lance Riek and Paul Zeman for their comments and assistance We would also like to thank Khristina Nathan our most unbiased and critical reviewer for his suggestions for improving the content and accuracy of this text A special thanks goes to Steve Hoffman who was instrumental in helping us perform the experiments described in Chapter 9

Hardware Annealing in Analog VLSI Neurocomputing Bank W. Lee, Bing J. Sheu, 1990-12-31 Rapid advances in neural sciences and VLSI design technologies have provided an excellent means to boost the computational capability and efficiency of data and signal processing tasks by several orders of magnitude With massively parallel processing capabilities artificial neural networks can be used to solve many engineering and scientific problems Due to the optimized data communication structure for artificial intelligence applications a neurocomputer is considered as the most promising sixth generation computing machine Typical applications of artificial neural networks include associative memory pattern classification early vision processing speech recognition image data compression and intelligent robot control VLSI neural circuits play an

important role in exploring and exploiting the rich properties of artificial neural networks by using programmable synapses and gain adjustable neurons Basic building blocks of the analog VLSI neural networks consist of operational amplifiers as electronic neurons and synthesized resistors as electronic synapses The synapse weight information can be stored in the dynamically refreshed capacitors for medium term storage or in the floating gate of an EEPROM cell for long term storage The feedback path in the amplifier can continuously change the output neuron operation from the unity gain configuration to a high gain configuration The adjustability of the voltage gain in the output neurons allows the implementation of hardware annealing in analog VLSI neural chips to find optimal solutions very efficiently Both supervised learning and unsupervised learning can be implemented by using the programmable neural chips

Proceedings ,1991 The Cumulative Book Index ,1991 A world list of books in the English language *Books in Print Supplement* ,1994 The Fourth Conference on Artificial Intelligence Applications ,1988

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Table of Contents Automatic Programming Applied To Vlsi Cad Software

1. Understanding the eBook Automatic Programming Applied To Vlsi Cad Software
 - The Rise of Digital Reading Automatic Programming Applied To Vlsi Cad Software
 - Advantages of eBooks Over Traditional Books
2. Identifying Automatic Programming Applied To Vlsi Cad Software
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Automatic Programming Applied To Vlsi Cad Software
 - User-Friendly Interface
4. Exploring eBook Recommendations from Automatic Programming Applied To Vlsi Cad Software
 - Personalized Recommendations
 - Automatic Programming Applied To Vlsi Cad Software User Reviews and Ratings

- Automatic Programming Applied To Vlsi Cad Software and Bestseller Lists
- 5. Accessing Automatic Programming Applied To Vlsi Cad Software Free and Paid eBooks
 - Automatic Programming Applied To Vlsi Cad Software Public Domain eBooks
 - Automatic Programming Applied To Vlsi Cad Software eBook Subscription Services
 - Automatic Programming Applied To Vlsi Cad Software Budget-Friendly Options
- 6. Navigating Automatic Programming Applied To Vlsi Cad Software eBook Formats
 - ePub, PDF, MOBI, and More
 - Automatic Programming Applied To Vlsi Cad Software Compatibility with Devices
 - Automatic Programming Applied To Vlsi Cad Software Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Automatic Programming Applied To Vlsi Cad Software
 - Highlighting and Note-Taking Automatic Programming Applied To Vlsi Cad Software
 - Interactive Elements Automatic Programming Applied To Vlsi Cad Software
- 8. Staying Engaged with Automatic Programming Applied To Vlsi Cad Software
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Automatic Programming Applied To Vlsi Cad Software
- 9. Balancing eBooks and Physical Books Automatic Programming Applied To Vlsi Cad Software
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Automatic Programming Applied To Vlsi Cad Software
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Automatic Programming Applied To Vlsi Cad Software
 - Setting Reading Goals Automatic Programming Applied To Vlsi Cad Software
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Automatic Programming Applied To Vlsi Cad Software
 - Fact-Checking eBook Content of Automatic Programming Applied To Vlsi Cad Software
 - Distinguishing Credible Sources

13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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