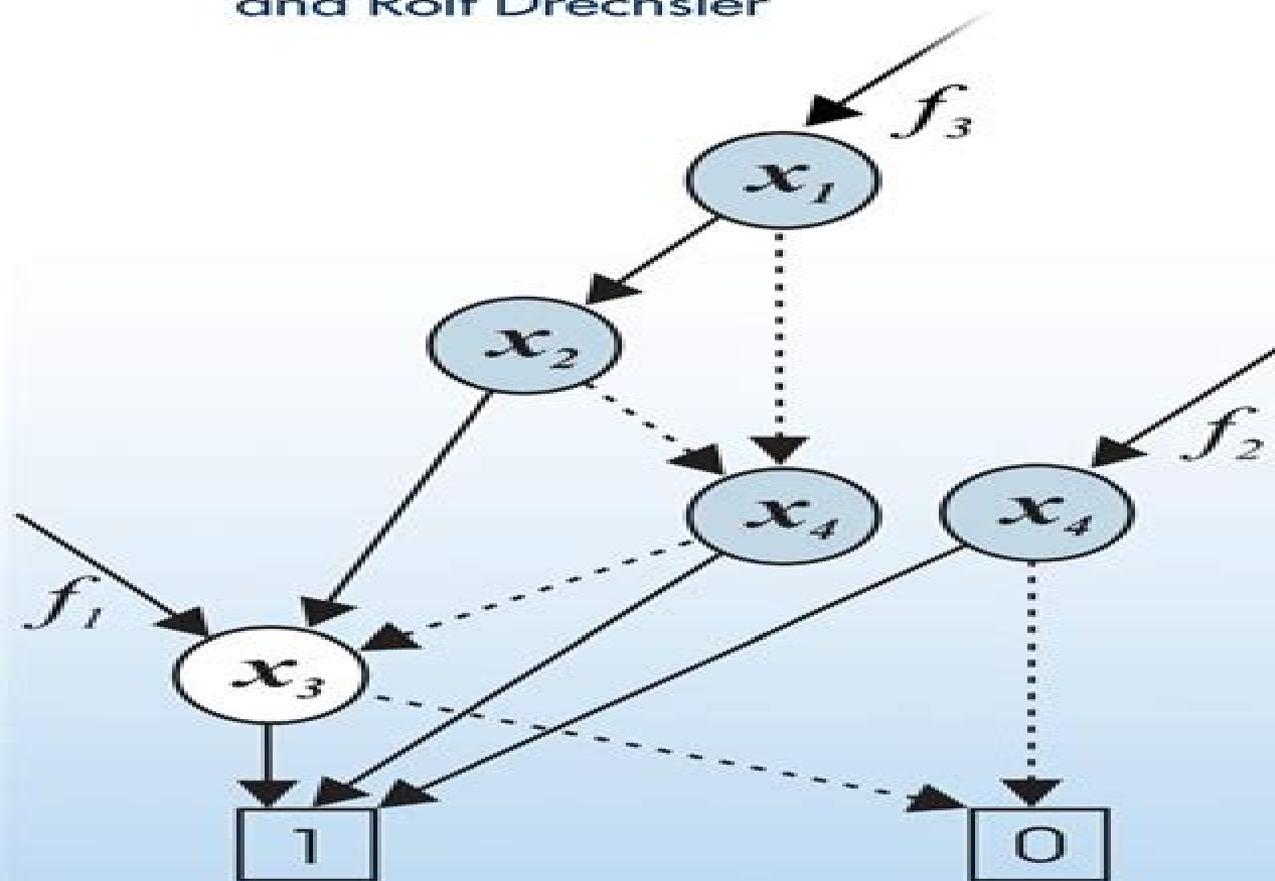


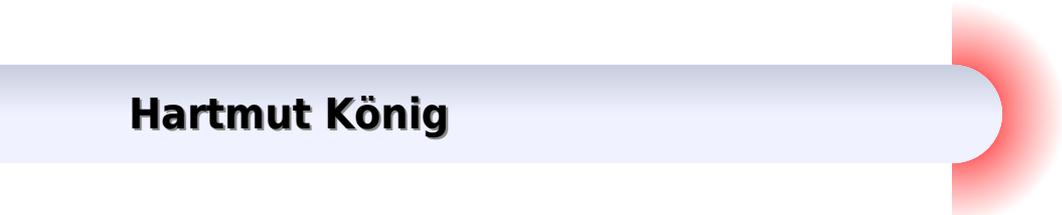
# Advanced BDD Optimization

Rüdiger Ebendt,  
Görschwin Fey  
and Rolf Drechsler



# Advanced Bdd Optimization

**Hartmut König**



## **Advanced Bdd Optimization:**

**Advanced BDD Optimization** Rudiger Ebdndt,Görschwin Fey,Rolf Drechsler,2005-12-05 VLSI CADhas greatly benefited from the use of reduced ordered Binary Decision Diagrams BDDs and the clausal representation as a problem of Boolean Satisfiability SAT e.g in logic synthesis verification or design for testability In recent practical applications BDDs are optimized with respect to new objective functions for design space exploration The latest trends show a growing number of proposals to fuse the concepts of BDD and SAT This book gives a modern presentation of the established as well as of recent concepts Latest results in BDD optimization are given covering different aspects of paths in BDDs and the use of efficient lower bounds during optimization The presented algorithms include Branch and Bound and the generic A algorithm as efficient techniques to explore large search spaces The A algorithm originates from Artificial Intelligence AI and the EDA community has been unaware of this concept for a long time Recently the A algorithm has been introduced as a new paradigm to explore design spaces in VLSI CAD Besides AI search techniques the book also discusses the relation to another field of activity bordered to VLSI CAD and BDD optimization the clausal representation as a SAT problem In-Memory Computing Saeideh

Shirinzadeh,Rolf Drechsler,2019-05-22 This book describes a comprehensive approach for synthesis and optimization of logic in memory computing hardware and architectures using memristive devices which creates a firm foundation for practical applications Readers will get familiar with a new generation of computer architectures that potentially can perform faster as the necessity for communication between the processor and memory is surpassed The discussion includes various synthesis methodologies and optimization algorithms targeting implementation cost metrics including latency and area overhead as well as the reliability issue caused by short memory lifetime Presents a comprehensive synthesis flow for the emerging field of logic in memory computing Describes automated compilation of programmable logic in memory computer architectures Includes several effective optimization algorithm also applicable to classical logic synthesis Investigates unbalanced write traffic in logic in memory architectures and describes wear leveling approaches to alleviate it **Theoretical Computer**

**Science** Josep Diaz,Ivan Lanese,Davide Sangiorgi,2014-08-23 This book constitutes the refereed proceedings of the 8th FIP WG 2.2 International Conference TCS 2014 held in Rome Italy in September 2014 The 26 revised full papers presented together with two invited talks were carefully reviewed and selected from 73 submissions Suggestion please check and add more if needed TCS 2014 consisted of two tracks with separate program committees which dealt respectively with Track A Algorithms Complexity and Models of Computation and Track B Logic Semantics Specification and Verification

**Service-Oriented Computing** Chi-Hung Chi,2009-11-17 This book constitutes the proceedings of the 7th Joint International Conference on Service Oriented Computing ICSOC ServiceWave 2009 held in Stockholm Sweden in November 2009 The 54 contributions to this volume consisting of 37 full papers 8 short papers and 9 demonstration papers were carefully reviewed and selected from 228 submissions The papers are arranged in topical sections on composition discovery

design principles customization and adaptation negotiation agreements and compliance selection platforms and infrastructures security modeling and design validation and verification reputation and ranking and service management This volume launches the new subline of Lecture Notes in Computer Science entitled LNCS Services Science

**Innovative Applications Of Information Technology For The Developing World - Proceedings Of The 3rd Asian Applied Computing Conference (Aacc 2005)** Hirendra Man Pradhan,Sudha Jha,Lalit M Patnaik,Asoke K Talukder,Deepak Bhattarai,S Sitharama Iyenger,2007-07-02 Information and communication technology ICT has become a generic and indispensable tool for addressing and solving problems in such diverse areas as management social and health services transportation security and education As the cost of equipment drops dramatically it also becomes widely accessible in the developing countries However problems of high costs for adequate training of personnel access to state to the art software and the consultancies needed to facilitate access to ICT can constitute highly dissuasive factors in the dissemination of ICT in developing countries This volume describes a series of successful initiatives for the insertion of ICT in developing economies It also identifies significant problems that are likely to be encountered and suggests useful solutions to these problems It therefore serves as a useful tool for example applications and for the successful assimilation of these technologies in developing societies and countries a

**Service-Oriented Computing** Chengfei Liu,Heiko Ludwig,Farouk Toumani,Qi Yu,2012-10-26 This book constitutes the conference proceedings of the 10th International Conference on Service Oriented Computing ICSOC 2012 held in Shanghai China in November 2012 The 32 full papers and 21 short papers presented were carefully reviewed and selected from 185 submissions The papers are organized in topical sections on service engineering service management cloud service QoS service security privacy and personalization service applications in business and society service composition and choreography service scaling and cloud process management service description and discovery service security privacy and personalization applications as well as cloud computing

**Technology Mapping for LUT-Based FPGA** Marcin Kubica,Adam Opara,Dariusz Kania,2020-11-07 This book covers selected topics of automated logic synthesis dedicated to FPGAs The authors focused on two main problems decomposition of the multioutput functions and technology mapping Additionally the idea of using binary decision diagrams BDD in these processes was presented The book is a scientific monograph summarizing the authors many years of research As a result it contains a large number of experimental results which makes it a valuable source for other researchers The book has a significant didactic value Its arrangement allows for a gradual transition from basic things e g description of logic functions to much more complex issues This approach allows less advanced readers to better understand the described problems In addition the authors made sure that the issues described in the book were supported by practical examples thanks to which the reader can independently analyze even the most complex problems described in the book

**Compact Representations for the Design of Quantum Logic** Philipp Niemann,Robert Wille,2017-08-21 This book discusses modern approaches and challenges of

computer aided design CAD of quantum circuits with a view to providing compact representations of quantum functionality Focusing on the issue of quantum functionality it presents Quantum Multiple Valued Decision Diagrams QMDDs a means of compactly and efficiently representing and manipulating quantum logic For future quantum computers going well beyond the size of present day prototypes the manual design of quantum circuits that realize a given quantum functionality on these devices is no longer an option In order to keep up with the technological advances methods need to be provided which similar to the design and synthesis of conventional circuits automatically generate a circuit description of the desired functionality To this end an efficient representation of the desired quantum functionality is of the essence While straightforward representations are restricted due to their exponentially large matrix descriptions and other decision diagram like structures for quantum logic suffer from not comprehensively supporting typical characteristics QMDDs employ a decomposition scheme that more naturally models quantum systems As a result QMDDs explicitly support quantum mechanical effects like phase shifts and are able to take more advantage of corresponding redundancies thereby allowing a very compact representation of relevant quantum functionality composed of dozens of qubits This provides the basis for the development of sophisticated design methods as shown for quantum circuit synthesis and verification Towards a Design Flow for Reversible Logic Robert Wille,Rolf Drechsler,2010-07-28 The development of computing machines found great success in the last decades But the ongoing miniaturization of integrated circuits will reach its limits in the near future Shrinking transistor sizes and power dissipation are the major barriers in the development of smaller and more powerful circuits Reversible logic provides an alternative that may overcome many of these problems in the future For low power design reversible logic offers significant advantages since zero power dissipation will only be possible if computation is reversible Furthermore quantum computation profits from enhancements in this area because every quantum circuit is inherently reversible and thus requires reversible descriptions However since reversible logic is subject to certain restrictions e.g fanout and feedback are not directly allowed the design of reversible circuits significantly differs from the design of traditional circuits Nearly all steps in the design flow like synthesis verification or debugging must be redeveloped so that they become applicable to reversible circuits as well But research in reversible logic is still at the beginning No continuous design flow exists so far In this book contributions to a design flow for reversible logic are represented This includes advanced methods for synthesis optimization verification and debugging Protocol Engineering Hartmut König,2012-10-09 Communication protocols form the operational basis of computer networks and telecommunication systems They are behavior conventions that describe how communication systems interact with each other defining the temporal order of the interactions and the formats of the data units exchanged essentially they determine the efficiency and reliability of computer networks Protocol Engineering is an important discipline covering the design validation and implementation of communication protocols Part I of this book is devoted to the fundamentals of communication protocols describing their working principles and implicitly

also those of computer networks The author introduces the concepts of service protocol layer and layered architecture and introduces the main elements required in the description of protocols using a model language He then presents the most important protocol functions Part II deals with the description of communication protocols offering an overview of the various formal methods the essence of Protocol Engineering The author introduces the fundamental description methods such as finite state machines Petri nets process calculi and temporal logics that are in part used as semantic models for formal description techniques He then introduces one representative technique for each of the main description approaches among others SDL and LOTOS and surveys the use of UML for describing protocols Part III covers the protocol life cycle and the most important development stages presenting the reader with approaches for systematic protocol design with various verification methods with the main implementation techniques and with strategies for their testing in particular with conformance and interoperability tests and the test description language TTCN The author uses the simple data transfer example protocol XDT eXample Data Transfer throughout the book as a reference protocol to exemplify the various description techniques and to demonstrate important validation and implementation approaches The book is an introduction to communication protocols and their development for undergraduate and graduate students of computer science and communication technology and it is also a suitable reference for engineers and programmers Most chapters contain exercises and the author's accompanying website provides further online material including a complete formal description of the XDT protocol and an animated simulation visualizing its behavior

**Proceedings of the ... ACM Great Lakes Symposium on VLSI**, 2006 Design Recovery for Combinational Logic Exploiting Boolean Relationships Travis Edward Doom, 1998

**Digest of Technical Papers**, 2001 **IEEE Circuits & Devices**, 1994 Heuristic and Exact Optimization of Reduced Ordered Binary Decision Diagrams (BDDs) Rüdiger Ebendt, 2004 Computer Aided Design of Very Large Scale Integrated Circuits VLSI CAD has greatly benefited from the use of reduced ordered Binary Decision Diagrams BDDs e g in logic synthesis verification or design for testability In recent methods BDDs are optimized with respect to new objective functions exploring the respective design spaces Today design space exploration and multi objective design goals are important for many applications New algorithms for BDD optimization have to be considered This book is dedicated to the idea of moving towards new optimization goals and design spaces different from those considered so far The book closes the gap between theory and practice by transferring the latest theoretical insights into recent practical applications In this a solid thorough analysis of the theory is presented which is completed by experimental studies The basic concepts have been known and understood for a short time and they will have wide impact on further developments in the field As logic synthesis verification and design for testability are standard topics in graduate courses the book also can be used as a text book for advanced courses in this field

**On Efficient Software Realization of Sequential Machines** Yunjian Jiang, 2003 *Proceedings of the ... International Symposium on Hardware/Software Codesign*, 2001 **Mathematical Foundations**

**of Computer Science** ,1994     *Adaptive Selection of Face Coarse Degrees of Freedom in the BDDC and the FETI-DP Iterative Substructuring Methods* Jan Mandel,2005 We propose a class of method for the adaptive selection of the coarse space of the BDDC Balancing Domain Decomposition by Constraints and FETI DP Finite Element Tearing and Interconnecting Dual Primal iterative substructuring methods The methods work by adding coarse degrees of freedom constructed from eigenvectors associated with intersections of selected pairs of adjacent substructures It is assumed that the starting coarse degrees of freedom are already sufficient to prevent relative rigid body motions in any selected pair of adjacent substructures A heuristic indicator of the the condition number is developed and a minimal number of coarse degrees of freedom is added to decrease the indicator under a given threshold It is shown numerically on 2D elasticity problems that the indicator based on pairs of substructures with common edges predicts the actual condition number reasonably well and that the method can select adaptively the hard part of the problem and concentrate computational work there to achieve good convergence of the iterations at a modest cost Authors abstract     **Asian Test Symposium** ,2004

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