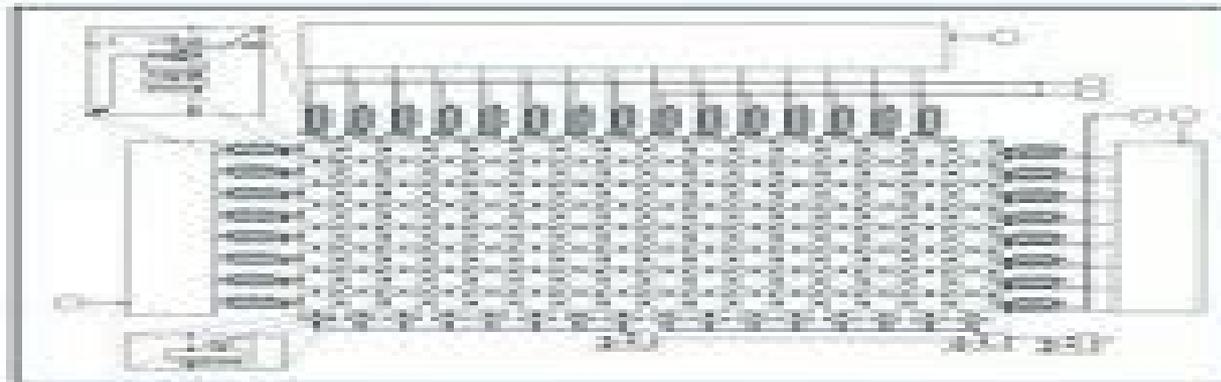


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# Application-Driven Architecture Synthesis

edited by

**Francky Catthoor**  
**Lars Svensson**



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# Application Driven Architecture Synthesis

**Filip Thoen, Francky Catthoor**



## **Application Driven Architecture Synthesis:**

**Application-Driven Architecture Synthesis** Francky Catthoor, Lars-Gunnar Svensson, 2012-12-06 Application Driven Architecture Synthesis describes the state of the art of architectural synthesis for complex real time processing In order to deal with the stringent timing requirements and the intricacies of complex real time signal and data processing target architecture styles and target application domains have been adopted to make the synthesis approach feasible These approaches are also heavily application driven which is illustrated by many realistic demonstrations used as examples in the book The focus is on domains where application specific solutions are attractive such as significant parts of audio telecom instrumentation speech robotics medical and automotive processing image and video processing TV multi media radar sonar Application Driven Architecture Synthesis is of interest to both academics and senior design engineers and CAD managers in industry It provides an excellent overview of what capabilities to expect from future practical design tools and includes an extensive bibliography

**Modeling, Verification and Exploration of Task-Level Concurrency in Real-Time Embedded Systems** Filip Thoen, Francky Catthoor, 2012-12-06 system is a complex object containing a significant percentage of electronics that interacts with the Real World physical environments humans etc through sensing and actuating devices A system is heterogeneous i e is characterized by the co existence of a large number of components of disparate type and function for example programmable components such as micro processors and Digital Signal Processors DSPs analog components such as A/D and D/A converters sensors transmitters and receivers Any approach to system design today must include software concerns to be viable In fact it is now common knowledge that more than 70% of the development cost for complex systems such as automotive electronics and communication systems are due to software development In addition this percentage is increasing constantly It has been my take for years that the so called hardware software co design problem is formulated at a too low level to yield significant results in shortening design time to the point needed for next generation electronic devices and systems The level of abstraction has to be raised to the Architecture Function co design problem where Function refers to the operations that the system is supposed to carry out and Architecture is the set of supporting components for that functionality The supporting components as we said above are heterogeneous and contain almost always programmable components

**The Fifth International Workshop on Rapid System Prototyping** Nick Kanopoulos, 1994 Proceedings of the June 1994 workshop presenting 25 papers on models for system simulation emulation in a hierarchical sense software to hardware mapping software prototyping and validation prototyping environments of hardware simulators and experiences from specific system prototyping projects Lacks an index Annotation copyright by Book News Inc Portland OR

**IEEE Workshop on Signal Processing Systems**, 1997 *Design of System on a Chip* Ricardo Reis, Jochen A.G. Jess, Jochen Jess, 2004-07-14 Design of System on a Chip is the first of two volumes addressing the design challenges associated with new generations of the semiconductor technology The various

chapters are the compilations of tutorials presented at workshops in Brazil in the recent years by prominent authors from all over the world In particular the first book deals with components and circuits Device models have to satisfy the conditions to be computationally economical in addition to be accurate and to scale over various generations of technology In addition the book addresses issues of the parasitic behavior of deep sub micron components such as parameter variations and sub threshold effects Furthermore various authors deal with items like mixed signal components and memories We wind up with an exposition of the technology problems to be solved if our community wants to maintain the pace of the International Technology Roadmap for Semiconductors ITRS

**Proceedings of the Fourth Euromicro Workshop on Parallel and Distributed Processing (PDP '96)**, 1996 Thirty nine papers and 32 posters from the January 1996 workshop assess the current status of parallel computing present recent developments and identify major trends More specifically they address technical issues connected with numerical algorithms communications programming tools parallel

**Proceedings**, 1994 Codesign Jerzy Rozenblit, Klaus Buchenrieder, 1995 **Computer Aided Systems Theory**, 1999 **EURO-DAC ...**, 1994 VLSI Signal Processing, VII Jan Rabaey, Paul M. Chau, John Eldon, 1994 **IEEE International Workshop on Rapid Systems Prototyping** IEEE Computer Society. Technical Committee on Simulation, 1999 Contains papers from a June 1999 workshop which brought together system designers model and tool developers integrated circuit designers and software engineers to explore problems and techniques in the area of rapid system prototyping Papers focus on models for system simulation emulation in a hierarchical sense software to hardware mapping software prototyping and validation prototyping environments of hardware simulators and experiences from specific system prototyping projects Contains sections on communication and distributed systems reconfigurable architectures reuse formal methods design methodologies interface technologies and FPGA based design Lacks a subject index Annotation copyrighted by Book News Inc Portland OR

*Highly Parallel Computations* M. P. Bekakos, 2001 This work provides a state of the art survey of the applications of processor network architectures and of various highly parallel computational methods Because of the increasing interdependence of the research activities in these major areas of highly parallel computations it seemed appropriate to produce this reference and study text The editor believes that the contributions in this work provide a valuable pointer to the research issues in this area Proceedings of the ... Midwest Symposium on Circuits and Systems, 1999 **Australian Computer Journal**, 1994 **The Second International Conference on Expert Systems for Development** IEEE Computer Society. TC on Distributed Processing, 1994 A number of the selected papers from the Second International Conference March 1994 represent interesting applications however because the areas of artificial intelligence and expert systems have grown to maturity during the five years since the first conference an even larger number of papers discuss the theoretical principles underlying the **18th International Conference on VLSI Design** International Conference on VLSI Design, 2005 The Cumulative Book Index, 1995 A world list of books in the English language *Digest of Technical*

*Papers ,2001 IEEE International Conference on Application-Specific Systems, Architectures and Processors* Lothar Thiele (Computer scientist),1997 These conference papers on computer hardware and design and testing include coverage of miscellaneous applications arithmetic algorithms and architectures DSP architectures systolic algorithms and architectures rapid prototyping design methodolgies and compilers

## **Application Driven Architecture Synthesis** Book Review: Unveiling the Magic of Language

In an electronic era where connections and knowledge reign supreme, the enchanting power of language has been apparent than ever. Its power to stir emotions, provoke thought, and instigate transformation is really remarkable. This extraordinary book, aptly titled "**Application Driven Architecture Synthesis**," written by a very acclaimed author, immerses readers in a captivating exploration of the significance of language and its profound impact on our existence. Throughout this critique, we shall delve into the book's central themes, evaluate its unique writing style, and assess its overall influence on its readership.

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