The Application of
Programmable
DSPs
in Mobile
Communications

Edited by
Alan Gatherer and **Edgar Auslander**

Application Of Programmable Dsps In Mobile Communications

IEEE Computer Society

Application Of Programmable Dsps In Mobile Communications:

The Application of Programmable DSPs in Mobile Communications Alan Gatherer,Edgar Auslander,2001-12-21 With the introduction of WAP in Europe and I mode in Japan mobile terminals took their first steps out of the world of mobile telephony and into the world of mobile data At the same time the shift from 2nd generation to 3rd generation cellular technology has increased the potential data rate available to mobile users by tenfold as well as shifting data transport from circuit switched to packet data These fundamental shifts in nature and the quantity of data available to mobile users has led to an explosion in the number of applications being developed for future digital terminal devices Though these applications are diverse they share a common need for complex Digital Signal Processing DSP and in most cases benefit from the use of programmable DSPs Digital Signal Processors Features contributions from experts who discuss the implementation and applications of programmable DSPs Includes detailed introductions to speech coding speech recognition video and audio compression biometric identification and their application for mobile communications devices Discusses the alternative DSP technology which is attempting to unseat the programmable DSP from the heart of tomorrow s mobile terminals Presents innovative new applications that are waiting to be discovered in the unique environment created when mobility meets signal processing The Application of Programmable DSPs in Mobile Communications provides an excellent overview for engineers moving into the area of mobile communications or entrepreneurs looking to understand state of the art in mobile terminals It is also a must for students and professors looking for new application areas where DSP technology is being applied

Low-Power Electronics Design Christian Piguet,2018-10-03 The power consumption of integrated circuits is one of the most problematic considerations affecting the design of high performance chips and portable devices The study of power saving design methodologies now must also include subjects such as systems on chips embedded software and the future of microelectronics Low Power Electronics Design covers all major aspects of low power design of ICs in deep submicron technologies and addresses emerging topics related to future design This volume explores in individual chapters written by expert authors the many low power techniques born during the past decade It also discusses the many different domains and disciplines that impact power consumption including processors complex circuits software CAD tools and energy sources and management The authors delve into what many specialists predict about the future by presenting techniques that are promising but are not yet reality They investigate nanotechnologies optical circuits ad hoc networks e textiles as well as human powered sources of energy Low Power Electronics Design delivers a complete picture of today s methods for reducing power and also illustrates the advances in chip design that may be commonplace 10 or 15 years from now   Low-Power Processors and Systems on Chips Christian Piguet,2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet s recently published Low Power Electronics Design this volume addresses the design of low power microprocessors in deep submicron

technologies It provides a focused reference for specialists involved in systems on chips from low power microprocessors to DSP cores reconfigurable processors memories ad hoc networks and embedded software Low Power Processors and Systems on Chips is organized into three broad sections for convenient access The first section examines the design of digital signal processors for embedded applications and techniques for reducing dynamic and static power at the electrical and system levels The second part describes several aspects of low power systems on chips including hardware and embedded software aspects efficient data storage networks on chips and applications such as routing strategies in wireless RF sensing and actuating devices The final section discusses embedded software issues including details on compilers retargetable compilers and coverification tools Providing detailed examinations contributed by leading experts Low Power Processors and Systems on Chips supplies authoritative information on how to maintain high performance while lowering power consumption in modern processors and SoCs It is a must read for anyone designing modern computers or embedded systems

Adaptation in Wireless Communications - 2 Volume Set Mohamed Ibnkahla,2018-10-08 The widespread use of adaptation techniques has helped to meet the increased demand for new applications From adaptive signal processing to cross layer design Adaptation in Wireless Communications covers all aspects of adaptation in wireless communications in a two volume set Each volume provides a unified framework for understanding adaptation and relates various specializations through common terminologies In addition to simplified state of the art cross layer design approaches they also describe advanced techniques such as adaptive resource management 4G communications and energy and mobility aware MAC protocols **Adaptive Signal Processing in Wireless Communications** Mohamed Ibnkahla,2017-12-19 Adaptive techniques play a key role in modern wireless communication systems The concept of adaptation is emphasized in the Adaptation in Wireless Communications Series through a unified framework across all layers of the wireless protocol stack ranging from the physical layer to the application layer and from cellular systems to next generation wireless networks This specific volume Adaptive Signal Processing in Wireless Communications is devoted to adaptation in the physical layer It gives an in depth survey of adaptive signal processing techniques used in current and future generations of wireless communication systems Featuring the work of leading international experts it covers adaptive channel modeling identification and equalization adaptive modulation and coding adaptive multiple input multiple output MIMO systems and cooperative diversity It also addresses other important aspects of adaptation in wireless communications such as hardware implementation reconfigurable processing and cognitive radio A second volume in the series Adaptation and Cross layer Design in Wireless Networks cat no 46039 is devoted to adaptation in the data link network and application layers

Field-programmable Logic and Applications ,2000   PLANS 2004 ,2004   Low-power VLSI Architectures for Finite Field Applications Leilei Song,1999   **Record** ,2004   *Proceedings* ,2001   **Emerging Commercial Mobile Wireless Technology and Standards** Phillip M. Feldman,1998 The U S Army as well as the other services is moving in the direction

of greater use of commercial technology and standards The principal motivation for this change is the desire to reduce costs However increased interoperability is another potential benefit This report evaluates commercial wireless communications technology including components and subsystems physical layer standards waveforms and signal processing protocol standards and products and services The author attempts to assess the suitability of these commercial technologies for Army tactical applications and to suggest the appropriate mix of commercial military unique and military variants of commercial systems for use on the digital battlefield The author recommends specific Army research and development areas where progress is needed in order to address voids between military requirements and currently available and emerging technology

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