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Circuit Design, Layout, and Simulation

R. JACOB BAKER

IEEE Series on Microelectronic Systems


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IEEE Circuits & Devices, 2006

Microelectronics Education B. Courtois, N. Guillemot, G. Kamarinos, G. Stéhelin, 2013-03-09 This is the third edition of the European Workshop on Microelectronics Education EWME A steady state regime has now been reached An international community of university teachers is constituted they exchange their experience and their pedagogical tools They discuss the best ways to transfer the rapidly changing techniques to their students and to introduce them to the new physical and mathematical concepts and models for the innovative techniques devices circuits and design methods The

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[DRAM Circuit Design](#) Brent Keeth, R. Jacob Baker, 2001 DRAM Circuit Design teaches readers the introductory level design of DRAM memory chips It focuses on giving readers a reference that can be used to educate students or practicing design engineers in DRAM circuit design

Proceedings of the ... Midwest Symposium on Circuits and Systems, 2001

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