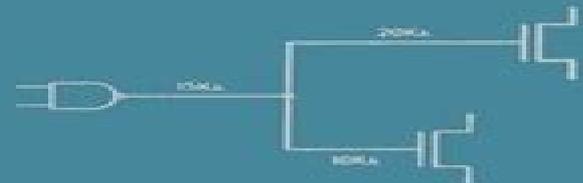


**Frederick J. Hill**  
**Gerald R. Peterson**

# **COMPUTER AIDED LOGICAL DESIGN WITH EMPHASIS ON VLSI**

**Fourth Edition**



# Computer Aided Logical Design With Emphasis On Vlsi

**John V. Oldfield, Richard C. Dorf**



## **Computer Aided Logical Design With Emphasis On Vlsi:**

Computer Aided Logical Design with Emphasis on VLSI Frederick J. Hill, Gerald R. Peterson, 1993-02-11 Tied to no particular set of computer aided logic design tools it advocates the new emphasis in VLSI design Includes support of layout synthesis from description in a register transfer level language as well as from design capture Contains a detailed introduction to Boolean algebra Karnaugh maps and sequential circuits In this edition discussion of combination logic has been extended switching circuits updated a comprehensive treatment of test generation for VLSI included **EBOOK:**

**Fundamentals of Digital Logic** Stephen Brown, Zvonko Vranesic, 2008-07-16 Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits The text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples which are easy to understand Then a modular approach is used to show how larger circuits are designed VHDL is a complex language so it is introduced gradually in the book Each VHDL feature is presented as it becomes pertinent for the circuits being discussed While it includes a discussion of VHDL the book provides thorough coverage of the fundamental concepts of logic circuit design independent of the use of VHDL and CAD tools A CD ROM containing all of the VHDL design examples used in the book as well as Altera's Quartus II CAD software is included free with every text *Digital System Design with SystemVerilog* Mark Zwolinski, 2009-10-23 The Definitive Up to Date Guide to Digital Design with SystemVerilog Concepts Techniques and Code To design state of the art digital hardware engineers first specify functionality in a high level Hardware Description Language HDL and today's most powerful useful HDL is SystemVerilog now an IEEE standard Digital System Design with SystemVerilog is the first comprehensive introduction to both SystemVerilog and the contemporary digital hardware design techniques used with it Building on the proven approach of his bestselling Digital System Design with VHDL Mark Zwolinski covers everything engineers need to know to automate the entire design process with SystemVerilog from modeling through functional simulation synthesis timing simulation and verification Zwolinski teaches through about a hundred and fifty practical examples each with carefully detailed syntax and enough in depth information to enable rapid hardware design and verification All examples are available for download from the book's companion Web site [zwolinski.org](http://zwolinski.org) Coverage includes Using electronic design automation tools with programmable logic and ASIC technologies Essential principles of Boolean algebra and combinational logic design with discussions of timing and hazards Core modeling techniques combinational building blocks buffers decoders encoders multiplexers adders and parity checkers Sequential building blocks latches flip flops registers counters memory and sequential multipliers Designing finite state machines from ASM chart to D flip flops next state and output logic Modeling interfaces and packages with SystemVerilog Designing testbenches architecture constrained random test generation and assertion based verification Describing RTL and FPGA synthesis models

Understanding and implementing Design for Test Exploring anomalous behavior in asynchronous sequential circuits  
Performing Verilog AMS and mixed signal modeling Whatever your experience with digital design older versions of Verilog or VHDL this book will help you discover SystemVerilog s full power and use it to the fullest The Electrical Engineering Handbook, Second Edition Richard C. Dorf,1997-09-26 In 1993 the first edition of The Electrical Engineering Handbook set a new standard for breadth and depth of coverage in an engineering reference work Now this classic has been substantially revised and updated to include the latest information on all the important topics in electrical engineering today Every electrical engineer should have an opportunity to expand his expertise with this definitive guide In a single volume this handbook provides a complete reference to answer the questions encountered by practicing engineers in industry government or academia This well organized book is divided into 12 major sections that encompass the entire field of electrical engineering including circuits signal processing electronics electromagnetics electrical effects and devices and energy and the emerging trends in the fields of communications digital devices computer engineering systems and biomedical engineering A compendium of physical chemical material and mathematical data completes this comprehensive resource Every major topic is thoroughly covered and every important concept is defined described and illustrated Conceptually challenging but carefully explained articles are equally valuable to the practicing engineer researchers and students A distinguished advisory board and contributors including many of the leading authors professors and researchers in the field today assist noted author and professor Richard Dorf in offering complete coverage of this rapidly expanding field No other single volume available today offers this combination of broad coverage and depth of exploration of the topics The Electrical Engineering Handbook will be an invaluable resource for electrical engineers for years to come **Applied Electromagnetics** Stuart M. Wentworth,2007-01-09 The revolution in wireless communications calls for a new focus in the electrical engineering curriculum Stuart M Wentworth fills that need with his new Applied Electromagnetics A Transmission Lines First Approach Incorporating the popular MATLAB program throughout it features practical applications for wireless systems transmission lines waveguides including optical fiber antennas and microwave systems Designed for use in a one or two semester sequence at the junior and senior level it offers students both detailed theoretical grounding and hands on experience in harmony with today s professional practice **Timing Optimization Through Clock Skew Scheduling** Ivan S. Kourtev,Baris Taskin,Eby G. Friedman,2008-11-16 History of the Book The last three decades have witnessed an explosive development in tegrated circuit fabrication technologies The complexities of current CMOS circuits are reaching beyond the 65 nanometer feature size and multi hundred million transistors per integrated circuit To fully exploit this technological potential circuit designers use sophisticated Computer Aided Design CAD tools While supporting the talents of innumerable microelectronics engineers these CAD tools have become the enabling factor responsible for the succe ful design and implementation of thousands of high performance large scale integrated circuits This book a research monograph

originated from a body of doctoral dissertation research completed by the first author at the University of Rochester from 1994 to 1999 while under the supervision of Prof Eby G Friedman This research focuses on issues in the design of the clock distribution network in large scale high performance digital synchronous circuits and particularly on algorithms for non zero clock skew scheduling During the development of this research it became clear that incorporating timing issues into the successful integrated circuit design process is of fundamental importance particularly in that advanced theoretical developments in this area have been slow to reach the designers desktops The second edition of the book is enhanced by the body of doctoral dissertation research completed by the second author at the University of Pittsburgh from 2000 to 2005 under the supervision of Prof

**Integrated Circuits** Peter Shepherd, 1996-11-11 Integrated circuits have revolutionised the world of electronics and the associated areas of computing and communication In past years the tasks of designing manufacturing and testing these types of circuit were restricted to a few specialist engineers However within recent years the proliferation of computer tools and affordable access to IC manufacturing foundries has resulted in a substantial increase in the number of people designing ICs for the first time both in universities and colleges and in industry This book introduces the reader to all aspects of IC design manufacture and testing with a minimum of mathematics but with relevant examples at each stage It examines the overall design strategies the engineering trade offs and the advantages disadvantages and optimum applications of each available technology

*Evaluation of Computer Aided Software as a Space Analysis Tool for Outfit Unit Design and Planning* Patrick D. Cahill, 1995

*Digital System Design with VHDL* Mark Zwoliński, 2000 Electronic systems based on digital principles are becoming ubiquitous A good design approach to these systems is essential and a top down methodology is favoured Such an approach is vastly simplified by the use of computer modeling to describe the systems VHDL is a formal language which allows a designer to model the behaviours and structure of a digital circuit on a computer before implementation Digital System Design with VHDL is intended both for students on Digital Design courses and practitioners who would like to integrate digital design and VHDL synthesis in the workplace Its unique approach combines the principles of digital design with a guide to the use of VHDL Synthesis issues are discussed and practical guidelines are provided for improving simulation accuracy and performance Features a practical perspective is obtained by the inclusion of real life examples an emphasis on software engineering practices encourages clear coding and adequate documentation of the process demonstrates the effects of particular coding styles on synthesis and simulation efficiency covers the major VHDL standards includes an appendix with examples in Verilog

**Forthcoming Books** Rose Arny, 1992

*CMOS Digital Integrated Circuits* Sung-Mo Kang, Yusuf Leblebici, 1999 The second edition of this comprehensive text contains extensive revisions to reflect recent advances in technology and in circuit design practices Recognizing that the area of digital integrated circuit design is evolving at an increasingly fast pace every effort has been made to present state of the art material on all subjects covered in the book This book is primarily designed as a comprehensive text for senior level and first

year graduate level digital circuit design classes as well as a reference for practicing engineers in the areas of IC design and VLSI

**ACM Conference on Computer and Communications Security**, 2006

**Understanding Smart Sensors** Randy Frank, 1996 Two of the most important trends in sensor development in recent years have been advances in micromachined sensing elements of all kinds and the increase in intelligence applied at the sensor level This book addresses both and provides a good overview of current technology I CS

**The Cumulative Book Index**, 1994 A world list of books in the English language

Computer Organization V. Carl Hamacher, Zvonko G. Vranesic, Safwat G. Zaky, 2002 This book provides comprehensive coverage of computer organization It presents hardware design principles and show how hardware design is influenced by the requirements of software

**Encyclopedia of Library and Information Science** Allen Kent, Harold Lancour, William Z. Nasri, Jay Elwood Daily, 1968 Vol 73 index to v 48 72

**Field-Programmable Gate Arrays** John V. Oldfield, Richard C. Dorf, 1995-01-23 Due to unique advantages like security improved testing and reprogrammability field programmable gate arrays are making broad inroads in the electronics industry This comprehensive overview of the topic explains the underlying principles strengths and limitations of a range of FPGA architectures Includes abundant references and illustrations

*ASEE Prism*, 1992

**ICSEE '95** Magdy F. Iskander, Charles Eugene Knadler, 1995

**Proceedings** Euromicro. Conference, 1998

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