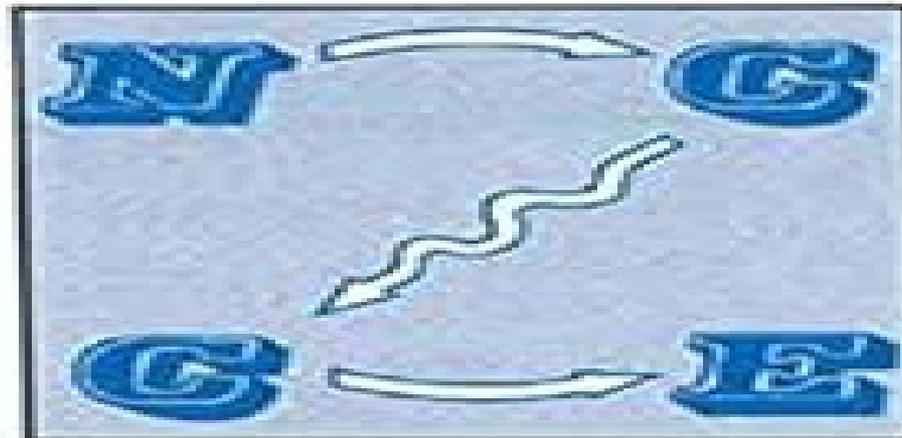

Computational Electronics

Semiconductor Transport
and Device Simulation

edited by

K. Hess
J.P. Leburton
U. Ravaioli



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Computational Electronics Semiconductor Transport And Device Simulation

SJ Ball



Computational Electronics Semiconductor Transport And Device Simulation:

Computational Electronics Karl Hess, J.P. Leburton, U. Ravaioli, 2013-03-14 Large computational resources are of ever increasing importance for the simulation of semiconductor processes devices and integrated circuits The Workshop on Computational Electronics was intended to be a forum for the discussion of the state of the art of device simulation Three major research areas were covered conventional simulations based on the drift diffusion and the hydrodynamic models Monte Carlo methods and other techniques for the solution of the Boltzmann transport equation and computational approaches to quantum transport which are relevant to novel devices based on quantum interference and resonant tunneling phenomena Our goal was to bring together researchers from various disciplines that contribute to the advancement of device simulation These include Computer Science Electrical Engineering Applied Physics and Applied Mathematics The success of this multidisciplinary formula was proven by numerous interactions which took place at the Workshop and during the following three day Short Course on Computational Electronics The format of the course including a number of tutorial lectures and the large attendance of graduate students stimulated many discussions and has proven to us once more the importance of cross fertilization between the different disciplines *Computational Electronics*, 1991

Computational Electronics Dragica Vasileska, Stephen M. Goodnick, 2022-06-01 Computational Electronics is devoted to state of the art numerical techniques and physical models used in the simulation of semiconductor devices from a semiclassical perspective Computational electronics as a part of the general Technology Computer Aided Design TCAD field has become increasingly important as the cost of semiconductor manufacturing has grown exponentially with a concurrent need to reduce the time from design to manufacture The motivation for this volume is the need within the modeling and simulation community for a comprehensive text which spans basic drift diffusion modeling through energy balance and hydrodynamic models and finally particle based simulation One unique feature of this book is a specific focus on numerical examples particularly the use of commercially available software in the TCAD community The concept for this book originated from a first year graduate course on computational electronics taught now for several years in the Electrical Engineering Department at Arizona State University Numerous exercises and projects were derived from this course and have been included The prerequisite knowledge is a fundamental understanding of basic semiconductor physics the physical models for various device technologies such as pn diodes bipolar junction transistors and field effect transistors

Computational Electronics Dragica Vasileska, 2010 Starting with the simplest semiclassical approaches and ending with the description of complex fully quantum mechanical methods for quantum transport analysis of state of the art devices Computational Electronics Semiclassical and Quantum Device Modeling and Simulation provides a comprehensive overview of the essential techniques and methods for effectively analyzing transport in semiconductor devices With the transistor reaching its limits and new device designs and paradigms of operation being explored this timely resource delivers the simulation methods needed to

properly model state of [Advanced Physics of Electron Transport in Semiconductors and Nanostructures](#) Massimo V. Fischetti, William G. Vandenberghe, 2016-05-20 This textbook is aimed at second year graduate students in Physics Electrical Engineering or Materials Science It presents a rigorous introduction to electronic transport in solids especially at the nanometer scale Understanding electronic transport in solids requires some basic knowledge of Hamiltonian Classical Mechanics Quantum Mechanics Condensed Matter Theory and Statistical Mechanics Hence this book discusses those sub topics which are required to deal with electronic transport in a single self contained course This will be useful for students who intend to work in academia or the nano micro electronics industry Further topics covered include the theory of energy bands in crystals of second quantization and elementary excitations in solids of the dielectric properties of semiconductors with an emphasis on dielectric screening and coupled interfacial modes of electron scattering with phonons plasmons electrons and photons of the derivation of transport equations in semiconductors and semiconductor nanostructures somewhat at the quantum level but mainly at the semi classical level The text presents examples relevant to current research thus not only about Si but also about III V compound semiconductors nanowires graphene and graphene nanoribbons In particular the text gives major emphasis to plane wave methods applied to the electronic structure of solids both DFT and empirical pseudopotentials always paying attention to their effects on electronic transport and its numerical treatment The core of the text is electronic transport with ample discussions of the transport equations derived both in the quantum picture the Liouville von Neumann equation and semi classically the Boltzmann transport equation BTE An advanced chapter Chapter 18 is strictly related to the tricky transition from the time reversible Liouville von Neumann equation to the time irreversible Green s functions to the density matrix formalism and classically to the Boltzmann transport equation Finally several methods for solving the BTE are also reviewed including the method of moments iterative methods direct matrix inversion Cellular Automata and Monte Carlo Four appendices complete the text **Nano-Electronic Devices** Dragica Vasileska, Professor Stephen M Goodnick, 2011-06-12 This exhaustive survey of advanced simulation methods for modeling nanoscale devices systematically covers both theoretical approaches and numerical solutions links methodology with the type of device and includes advice on state of the art semiconductors [Modelling of Interface Carrier Transport for Device Simulation](#) Dietmar Schroeder, 2012-11-01 This book contains a comprehensive review of the physics modelling and simulation of electron transport at interfaces in semiconductor devices It combines a review of existing interface charge transport models with original developments and introduces a unified representation of charge transport at semiconductor interfaces [Monte Carlo Device Simulation](#) Karl Hess, 2012-12-06 Monte Carlo simulation is now a well established method for studying semiconductor devices and is particularly well suited to highlighting physical mechanisms and exploring material properties Not surprisingly the more completely the material properties are built into the simulation up to and including the use of a full band structure the more powerful is the method Indeed it is now becoming increasingly clear that

phenomena such as reliability related hot electron effects in MOSFETs cannot be understood satisfactorily without using full band Monte Carlo The IBM simulator DAMOCLES therefore represents a landmark of great significance DAMOCLES sums up the total of Monte Carlo device modeling experience of the past and reaches with its capabilities and opportunities into the distant future This book therefore begins with a description of the IBM simulator The second chapter gives an advanced introduction to the physical basis for Monte Carlo simulations and an outlook on why complex effects such as collisional broadening and intracollisional field effects can be important and how they can be included in the simulations References to more basic introductory material can be found throughout describes a typical relationship of Monte Carlo simulations to experimental data and indicates a major difficulty the vast number of deformation potentials required to simulate transport throughout the entire Brillouin zone The fourth chapter addresses possible further extensions of the Monte Carlo approach and subtleties of the electron-electron interaction

Simulation of Transport in Nanodevices François Triozon, Philippe Dollfus, 2016-11-22 Linear current-voltage pattern has been and continues to be the basis for characterizing, evaluating performance and designing integrated circuits but is shown not to hold its supremacy as channel lengths are being scaled down In a nanoscale circuit with reduced dimensionality in one or more of the three Cartesian directions quantum effects transform the carrier statistics In the high electric field the collision-free ballistic transport is predicted while in low electric field the transport remains predominantly scattering limited In a micro-nano circuit even a low logic voltage of 1 V is above the critical voltage triggering nonohmic behavior that results in ballistic current saturation A quantum emission may lower this ballistic velocity

Stochastic Approaches to Electron Transport in Micro- and Nanostructures Mihail Nedjalkov, Ivan Dimov, Siegfried Selberherr, 2021-04-05 The book serves as a synergistic link between the development of mathematical models and the emergence of stochastic Monte Carlo methods applied for the simulation of current transport in electronic devices Regarding the models the historical evolution path beginning from the classical charge carrier transport models for microelectronics to current quantum-based nanoelectronics is explicated Accordingly the solution methods are elucidated from the early phenomenological single-particle algorithms applicable for stationary homogeneous physical conditions up to the complex algorithms required for quantum transport based on particle generation and annihilation The book fills the gap between monographs focusing on the development of the theory and the physical aspects of models their application and their solution methods and monographs dealing with the purely theoretical approaches for finding stochastic solutions of Fredholm integral equations

Physics and Simulation of Optoelectronic Devices, 2000

Nano-Electronic Devices Dragica Vasileska, Stephen M. Goodnick, 2011-06-10 This book surveys the advanced simulation methods needed for proper modeling of state-of-the-art nanoscale devices It systematically describes theoretical approaches and the numerical solutions that are used in explaining the operation of both power devices as well as nanoscale devices It clearly explains for what types of devices a particular

method is suitable which is the most critical point that a researcher faces and has to decide upon when modeling semiconductor devices

Hierarchical Device Simulation Christoph Jungemann, Bernd Meinerzhagen, 2012-12-06 This book summarizes the research of more than a decade Its early motivation dates back to the eighties and to the memorable talks Dr C Moglestue FHG Freiburg gave on his Monte Carlo solutions of the Boltzmann transport equation at the NASECODE conferences in Ireland At that time numerical semiconductor device modeling basically implied the application of the drift diffusion model On the one hand those talks clearly showed the potential of the Monte Carlo model for an accurate description of many important transport issues that cannot adequately be addressed by the drift diffusion approximation On the other hand they also clearly demonstrated that at that time only very few experts were able to extract useful results from a Monte Carlo simulator With this background Monte Carlo research activities were started in 1986 at the University of Aachen RWTH Aachen Germany Different to many other Monte Carlo research groups the Monte Carlo research in Aachen took place in an environment of active drift diffusion and hydrodynamic model development

Circuit, Device and Process Simulation Graham F. Carey, 1996-06-19 This book presents for the first time a unified treatment of the physical processes mathematical models and numerical techniques for circuit device and process simulation At the macroscopic level linear and nonlinear circuit elements are introduced to yield a mathematical model of an integrated circuit Numerical techniques used to solve this coupled system of ODEs are described Microscopically current flow within a transistor is modeled using the drift diffusion and hydrodynamic PDE systems Finite difference and finite element methods for spatial discretizations are treated as are grid generation and refinement upwinding and multilevel schemes At the fabrication level physical processes such as diffusion oxidation and crystal growth are modeled using reaction diffusion convection equations These models require multistep integration techniques and Krylov projection methods for successful implementation Exercises programming assignments and an extensive bibliography are included to reinforce and extend the treatment

The Monte Carlo Method for Semiconductor Device Simulation Carlo Jacoboni, Paolo Lugli, 1989-10-30 This volume presents the application of the Monte Carlo method to the simulation of semiconductor devices reviewing the physics of transport in semiconductors followed by an introduction to the physics of semiconductor devices

Transactions of the Society for Computer Simulation, 2001

Carrier Transport in Nanoscale MOS Transistors Hideaki Tsuchiya, Yoshinari Kamakura, 2017-06-13 A comprehensive advanced level examination of the transport theory of nanoscale devices Provides advanced level material of electron transport in nanoscale devices from basic principles of quantum mechanics through to advanced theory and various numerical techniques for electron transport Combines several up to date theoretical and numerical approaches in a unified manner such as Wigner Boltzmann equation the recent progress of carrier transport research for nanoscale MOS transistors and quantum correction approximations The authors approach the subject in a logical and systematic way reflecting their extensive teaching and research backgrounds

Quantum Transport in Ultrasmall Devices David K. Ferry, Harold L.

Grubin, Carlo Jacoboni, A.-P. Jauho, 2012-12-06 The operation of semiconductor devices depends upon the use of electrical potential barriers such as gate depletion in controlling the carrier densities electrons and holes and their transport Although a successful device design is quite complicated and involves many aspects the device engineering is mostly to devise a best device design by defining optimal device structures and manipulating impurity profiles to obtain optimal control of the carrier flow through the device This becomes increasingly difficult as the device scale becomes smaller and smaller Since the introduction of integrated circuits the number of individual transistors on a single chip has doubled approximately every three years As the number of devices has grown the critical dimension of the smallest feature such as a gate length which is related to the transport length defining the channel has consequently declined The reduction of this design rule proceeds approximately by a factor of 1.4 each generation which means we will be using 0.1015 μm rules for the 4 Gb chips a decade from now If we continue this extrapolation current technology will require 30 nm design rules and a cell 3.2 size

Zeitschrift Für Kristallographie ,1993 **Modeling and Simulation** ,1991

Whispering the Secrets of Language: An Emotional Quest through **Computational Electronics Semiconductor Transport And Device Simulation**

In a digitally-driven world wherever monitors reign great and immediate conversation drowns out the subtleties of language, the profound secrets and mental subtleties hidden within phrases often go unheard. However, situated within the pages of **Computational Electronics Semiconductor Transport And Device Simulation** a captivating fictional prize pulsating with natural feelings, lies a fantastic journey waiting to be undertaken. Published by a skilled wordsmith, that wonderful opus attracts readers on an introspective journey, delicately unraveling the veiled truths and profound affect resonating within the very cloth of each and every word. Within the emotional depths with this moving review, we shall embark upon a heartfelt exploration of the book is core styles, dissect their charming publishing type, and yield to the powerful resonance it evokes deep within the recesses of readers hearts.

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