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Comprehensive Functional Verification Bruce Wile, John Goss, Wolfgang Roesner, 2005-05-26 One of the biggest challenges in chip and system design is determining whether the hardware works correctly That is the job of functional verification engineers and they are the audience for this comprehensive text from three top industry professionals As designs increase in complexity so has the value of verification engineers within the hardware design team In fact the need for skilled verification engineers has grown dramatically functional verification now consumes between 40 and 70% of a project s labor and about half its cost Currently there are very few books on verification for engineers and none that cover the subject as comprehensively as this text A key strength of this book is that it describes the entire verification cycle and details each stage The organization of the book follows the cycle demonstrating how functional verification engages all aspects of the overall design effort and how individual cycle stages relate to the larger design process Throughout the text the authors leverage their 35 plus years experience in functional verification providing examples and case studies and focusing on the skills methods and tools needed to complete each verification task Comprehensive overview of the complete verification cycle Combines industry experience with a strong emphasis on functional verification fundamentals Includes real world case studies

Digital Design (VHDL) Peter J. Ashenden, 2007-10-24 Digital Design An Embedded Systems Approach Using VHDL provides a foundation in digital design for students in computer engineering electrical engineering and computer science courses It takes an up to date and modern approach of presenting digital logic design as an activity in a larger systems design context Rather than focus on aspects of digital design that have little relevance in a realistic design context this book concentrates on modern and evolving knowledge and design skills Hardware description language HDL based design and verification is emphasized VHDL examples are used extensively throughout By treating digital logic as part of embedded systems design this book provides an understanding of the hardware needed in the analysis and design of systems comprising both hardware and software components Includes a Web site with links to vendor tools labs and tutorials Presents digital logic design as an activity in a larger systems design context Features extensive use of VHDL examples to demonstrate HDL hardware description language usage at the abstract behavioural level and register transfer level as well as for low level verification and verification environments Includes worked examples throughout to enhance the reader s understanding and retention of the material Companion Web site includes links to tools for FPGA design from Synplicity Mentor Graphics and Xilinx VHDL source code for all the examples in the book lecture slides laboratory projects and solutions to exercises

Applied Computational Intelligence and Mathematical Methods Radek Silhavy, Petr Silhavy, Zdenka Prokopova, 2017-09-04 The book discusses real world problems and exploratory research in computational intelligence and mathematical models It brings new approaches and methods to real world problems and exploratory research that describes novel approaches in the mathematical methods computational intelligence methods and software engineering in the scope of

the intelligent systems This book constitutes the refereed proceedings of the Computational Methods in Systems and Software 2017 a conference that provided an international forum for the discussion of the latest high quality research results in all areas related to computational methods statistics cybernetics and software engineering

Hybrid Optimization Pascal van Hentenryck, Michela Milano, 2010-11-05 Hybrid Optimization focuses on the application of artificial intelligence and operations research techniques to constraint programming for solving combinatorial optimization problems This book covers the most relevant topics investigated in the last ten years by leading experts in the field and speculates about future directions for research This book includes contributions by experts from different but related areas of research including constraint programming decision theory operations research SAT artificial intelligence as well as others These diverse perspectives are actively combined and contrasted in order to evaluate their relative advantages This volume presents techniques for hybrid modeling integrated solving strategies including global constraints decomposition techniques use of relaxations and search strategies including tree search local search and metaheuristics Various applications of the techniques presented as well as supplementary computational tools are also discussed

Post-Silicon and Runtime Verification for Modern Processors Ilya Wagner, Valeria Bertacco, 2010-11-25 The purpose of this book is to survey the state of the art and evolving directions in post silicon and runtime verification The authors start by giving an overview of the state of the art in verification particularly current post silicon methodologies in use in the industry both for the domain of processor pipeline design and for memory subsystems They then dive into the presentation of several new post silicon verification solutions aimed at boosting the verification coverage of modern processors dedicating several chapters to this topic The presentation of runtime verification solutions follows a similar approach This is an area of processor design that is still in its early stages of exploration and that holds the promise of accomplishing the ultimate goal of achieving complete correctness guarantees for microprocessor based computation The authors conclude the book with a look towards the future of late stage verification and its growing role in the processor life cycle

Thông báo sách mới, 2009

Standardized Functional Verification Alan Wiemann, 2007-10-23 Standardized Functional Verification describes the science of functional verification that applies to any digital hardware system With a precise and comprehensive terminology this book describes a thorough technical framework for achieving superior results with greater efficiency It also defines a scientific basis for achieving functional closure and shows how true functional closure can be measured by software The author provides a wealth of practical guidance to the practicing professional It describes how to compare results from differing projects and how to assess the risk of functional bugs at tape out or at any other critical product juncture The book also shows consumers how to compare IP offerings from multiple vendors For producers it describes how to verify IP in a manner that meets customer needs Producers and consumers of silicon IP therefore have a solid framework to communicate requirements and to align expectations effectively and efficiently Standardized Functional Verification is a valuable reference for verification

engineers and managers and also for developers of verification software The principles and theory this book describes will drive creation of the next generation of verification tools *Functional Verification Coverage Measurement and Analysis* Andrew Piziali,2007-10-04 This book addresses a means of quantitatively assessing functional verification progress Without this process design and verification engineers and their management are left guessing whether or not they have completed verifying the device they are designing Using the techniques described in this book they will learn how to build a toolset which allows them to know how close they are to functional closure This is the first book to introduce a useful taxonomy for coverage of metric classification Using this taxonomy the reader will clearly understand the process of creating an effective coverage model This book offers a thoughtful and comprehensive treatment of its subject for anybody who is really serious about functional verification **Application of Control Theory in the Automotive Industry** M. A. Dorgham,1983 The Functional Verification of Electronic Systems Brian Bailey,2005-01-30 Addressing the need for full and accurate functional information during the design process this guide offers a comprehensive overview of functional verification from the points of view of leading experts at work in the electronic design industry Proceedings of the ... Conference .. Insurance Accounting and Statistical Association,1979 *Instrumentation in the Power Industry* ,1990 **Proceedings** ,1997 **10th Anniversary Symposium on Space Nuclear Power and Propulsion** Mohamed S. El-Genk,1993 **Standard & Poor's Stock Reports** ,2008-05 Third IEEE International Software Engineering Standards Symposium and Forum (ISESS 97) ,1997 The proceedings from the June 1997 conference focusing on the effectiveness of software engineering standards and their future particularly in respect to critical systems The 35 selected tutorials technical papers panel discussions and workshops deal with aspects of software safety and compliance identifying software users software product measurement software and systems engineering fundamental principles and formal methods The keynote address features cautionary advice to standards writers engaged with the limited claims that can be made for software dependability Lacks an index Annotation copyrighted by Book News Inc Portland OR **FAO Fisheries Technical Paper** ,1998 **Civil-Comp 89** B. H. V. Topping,1989 Contained in this publication in two volumes are all the papers except those relating to artificial intelligence presented at CIVIL COMP89 19 21 September 1989 London **Tutorial, Software Configuration Management** William Bryan,Christopher Chadbourne,Stan Siegel,Stanley G. Siegel,1980 **Symposium on Space Nuclear Power and Propulsion** ,1993

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