

CODE OPTIMIZATION: EFFECTIVE MEMORY USAGE

```
push bp
mov bp, sp
xor ax, ax
call @_StackCheck$@Word /
word_2030, 08C83h
word_2032, 08851h
word_2034, 551Eh
word_2036, 3083h
word_2038

SystemInit / word_2032
WINCRInit / word_2036
mov si, word_2038
mov di, word_203A
call @$_plus$@RealTI / RealTI
push bx
push ax
call
```

- Be informed! Understand typical mistakes made by programmers
- Get practical skills and undocumented solutions for creating faster programs
- Eliminate problems with effective programming techniques
- Overclock the memory to reach maximum performance

KRIS KASPERSKY

alist

Code Optimization Effective Memory Usage

Amir Hussain, Mirjana Ivanovic



Code Optimization Effective Memory Usage:

Code Optimization Kris Kaspersky,2003 A guide to optimizing programs on the PC and Unix platforms this book covers the expediency of optimization and the methods to increase the speed of programs via optimization Discussed are typical mistakes made by programmers that lessen the performance of the system along with easily implemented solutions Detailed descriptions of the devices and mechanism of interaction of the computer components effective ways of programming and a technique for optimizing programs are provided Programmers will also learn how to effectively implement programming methods in a high level language that is usually done in assembler with particular attention given to the RAM subsystem The working principles of the RAM and the way in which it is coupled with the processor as well as a description of programming methods that allows programmers to overclock the memory to reach maximum performance are included Code Optimization: Effective Memory Usage Kris Kaspersky,2003 This book covers methods that can be used to increase the speed of programs via optimisation Typical mistakes made by programmers that lessen the performance of the system are discussed along with easily implemented solutions Detailed descriptions of the devices and mechanisms of interaction between the computer components effective ways of programming and a technique for optimising programs are provided Programmers also will learn how to effectively implement in a high level language programming methods that are usually done in the assembler Particular attention is given to the RAM and cache subsystems The working principles of RAM and the way in which it is coupled with the processor as well as descriptions of programming methods that allow programmers to over clock the memory to reach maximum performance are included It is oriented toward application programmers with some experience in C C programming we well as toward system programmers with knowledge of the assembly language However the optimisation techniques described here are not bound to any high level programming language Thus knowledge of C is required only for understanding the source code provided in this book This book also will be useful to hardware specialists and technicians especially those who build and fine tune computers It provides detailed descriptions of the hardware operating principles and discusses the bottlenecks of the most common hardware components **Enhancing Java EE Applications: Strategies for Performance and Scalability** Peter Jones,2025-01-09 Unlock the full potential of your Java EE applications with *Enhancing Java EE Applications Strategies for Performance and Scalability* This essential guide provides comprehensive coverage of techniques and strategies to boost the performance and scalability of your Java EE applications From architectural insights to advanced coding practices each chapter brims with expert advice and practical solutions to help you overcome performance and scalability challenges Learn how to profile and monitor applications to pinpoint bottlenecks optimize Java EE servers effectively interact with databases and implement robust concurrency and multithreading Explore caching strategies fine tune application code and master network communications within a Java EE context Additionally discover how to manage memory efficiently and deploy resources wisely to ensure

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Performance

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is actually a recycled asset being shuffled behind the curtain at lightning speed Understand the microscopic math keeping your favorite digital worlds from collapsing Discover how the relentless pursuit of hardware efficiency forces programmers to become masters of digital sleight of hand

Electronics, Communications and Networks IV Amir Hussain, Mirjana Ivanovic, 2015-07-01 The 4th International Conference on Electronic Communications and Networks CECNet2014 inherits the fruitfulness of the past three conferences and lays a foundation for the forthcoming next year in Shanghai CECNet2014 was hosted by Hubei University of Science and Technology China with the main objective of providing a comprehensive global forum for experts and participants from academia to exchange ideas and presenting results of ongoing research in the most state of the art areas of Consumer Electronics Technology Communication Engineering and Technology Wireless Communications Engineering and Technology and Computer Engineering and Technology In this event 13 famous scholars and Engineers have delivered the keynote speeches on their latest research including Prof Vijaykrishnan Narayanan a Fellow of the Institute of Electrical and Electronics Engineers Prof Han Chieh Chao the Director of the Computer Center for Ministry of Education Taiwan from September 2008 to July 2010 Prof Borko Furht the founder of the Journal of Multimedia Tools and Applications Prof Kevin Deng who served as Acting Director of Hong Kong APAS R D Center in 2010 and Prof Minho Jo the Professor of Department of Computer and Information Science Korea University

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