

University of California
Berkeley
College of Engineering
Department of Electrical Engineering
and Computer Science

Robert W. Brodersen
EECS 40

Analog Circuit Design

Lectures
on
SINGLE TRANSISTOR CIRCUITS

Two Stages of Analysis

S-1

STEP 1: DC ANALYSIS

to Region of Operation of Transistor
to DC Currents and Voltages

STEP 2: AC ANALYSIS

to Calculate Small Signal parameters

for i_c, v_o

to Perform 2-Port analysis

$R_{in}, R_{out}, A_v, G_m$

$(A_v = G_m R_{out})$

Two Part Analysis

S-1

Small Signal Analysis

Set independent sources to zero

$$R_{in} = \left. \frac{v_i}{i_i} \right|_{v_o=0}$$

$$R_{out} = \left. \frac{v_o}{i_o} \right|_{v_i=0}$$

$$A_v = \left. \frac{v_o}{v_i} \right|_{R_L=0}$$

Common Source (Inverter, Gain Stage)
(High Gain, Moderate R_{out})

S-2

Problem: Find V_B for EOS

$V_T = 1V$	$V_{DD} = 5V$
$k' = 40\mu A/V^2$	$R_D = 1k\Omega$
$\lambda = 0.01$	$\gamma = 0.1$

Graph showing v_o vs v_i with regions: Cutoff, Saturation, Linear. Edge of Saturation (EOS) is marked at $v_i = V_B$.

Analysis And Design Of Transistor Circuits

T. A. Dorey



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Transistor Circuit Analysis and Design John J. Corning,1965 Transistor Circuit Analysis and Design Franklin C. Fitchen,1960 Analysis and Design of Transistor Circuits Laurence Golden Cowles,1966 **Analysis and Design of Transistor Circuits** Laurence G. Cowles,1968 Practical Transistor Circuit Design and Analysis Gerald Earl Williams,1973 **Transistor Circuit Analysis and Design. 2.ed** F.C. Fitchen,1966 **Transistor Circuit Design** Laurence G. Cowles,1972 Transistor Circuit Analysis and Design ... Second Edition Franklin Charles FITCHEN,1966

Mosfet Modeling For Circuit Analysis And Design Carlos Galup-montoro,Marcio Cherem Schneider,2007-02-27 This is the first book dedicated to the next generation of MOSFET models Addressed to circuit designers with an in depth treatment that appeals to device specialists the book presents a fresh view of compact modeling having completely abandoned the regional modeling approach Both an overview of the basic physics theory required to build compact MOSFET models and a unified treatment of inversion charge and surface potential models are provided The needs of digital analog and RF designers as regards the availability of simple equations for circuit designs are taken into account Compact expressions for hand analysis or for automatic synthesis valid in all operating regions are presented throughout the book All the main expressions for computer simulation used in the new generation compact models are derived Since designers in advanced technologies are increasingly concerned with fluctuations the modeling of fluctuations is strongly emphasized A unified approach for both space matching and time noise fluctuations is introduced **Transistor Circuit Techniques** G. J. Ritchie,1993-01-01 Transistor Circuit Techniques Gordon J. Ritchie,2017-06-29 Thoroughly revised and updated this highly successful textbook guides students through the analysis and design of transistor circuits It covers a wide range of circuitry both linear and switching Transistor Circuit Techniques Discrete and Integrated provides students with an overview of fundamental qualitative circuit operation followed by an examination of analysis and design procedure It incorporates worked problems and design examples to illustrate the concepts This third edition includes two additional chapters on power amplifiers and power supplies which further develop many of the circuit design techniques introduced in earlier chapters Part of the Tutorial Guides in Electronic Engineering series this book is intended for first and second year undergraduate courses A complete text on its own it offers the added advantage of being cross referenced to other titles in the series It is an ideal textbook for both students and instructors Research Directed Toward Analysis and Design of Transistor Circuits A. W. CARLSON,TRANS WORLD AIRLINES INC KANSAS CITY MO.,1961 The use of switches operated to produce a duration modulated pulse train as a linear amplifier are discussed An analysis of the signal and distortion is presented It is shown for a given degree of distortion that modulating both leading and trailing edges of the pulses equally allows a lower pulse rate relative to the highest signal frequency than if only one edge is modulated The efficiency of a single switch SPST in converting power supplied into ac signal power has a maximum of 50% for lossless switch and sinusoidal signal Circuits

using 2 ideal lossless switches have efficiencies of 100% independent of signal amplitude degree of modulation Some experiments with transistor switches are described Author Analysis and Design of Analog Integrated Circuits Paul R. Gray, Robert G. Meyer, 1992-08-24 This edition combines the consideration of metal oxide semiconductors MOS and bipolar circuits into a unified treatment that also includes MOS bipolar connections made possible by BiCMOS technology Contains extensive use of SPICE especially as an integral part of many examples in the problem sets as a more accurate check on hand calculations and as a tool to examine complex circuit behavior beyond the scope of hand analysis Concerned largely with the design of integrated circuits a considerable amount of material is also included on applications *Electronic Circuit Analysis and Design* Donald A. Neamen, 2001 This junior level electronics text provides a foundation for analyzing and designing analog and digital electronic circuits Computer analysis and design are recognized as significant factors in electronics throughout the book The use of computer tools is presented carefully alongside the important hand analysis and calculations The author Don Neamen has many years experience as an engineering educator and an engineer His experience shines through each chapter of the book rich with realistic examples and practical rules of thumb The book is divided into three parts Part 1 covers semiconductor devices and basic circuit applications Part 2 covers more advanced topics in analog electronics and Part 3 considers digital electronic circuits Transistor Circuit Analysis and Design T. A. Dorey, 1965

Electronic Circuit Analysis and Design William Hart Hayt (Jr.), Gerold W. Neudeck, 1984 **U.S. Environmental Protection Agency Library System Book Catalog Holdings as of July 1973** United States. Environmental Protection Agency. Library Systems Branch, 1974 **Design Manual for Transistor Circuits** John Millar Carroll, 1961 *Student Exercises for Practical Transistor Circuit Design and Analysis* Gerald Earl Williams, 1973 Transistor Circuits and Applications Laurence G. Cowles, 1974

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Table of Contents Analysis And Design Of Transistor Circuits

1. Understanding the eBook Analysis And Design Of Transistor Circuits
 - The Rise of Digital Reading Analysis And Design Of Transistor Circuits
 - Advantages of eBooks Over Traditional Books
2. Identifying Analysis And Design Of Transistor Circuits
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Analysis And Design Of Transistor Circuits
 - User-Friendly Interface
4. Exploring eBook Recommendations from Analysis And Design Of Transistor Circuits
 - Personalized Recommendations
 - Analysis And Design Of Transistor Circuits User Reviews and Ratings
 - Analysis And Design Of Transistor Circuits and Bestseller Lists
5. Accessing Analysis And Design Of Transistor Circuits Free and Paid eBooks
 - Analysis And Design Of Transistor Circuits Public Domain eBooks
 - Analysis And Design Of Transistor Circuits eBook Subscription Services
 - Analysis And Design Of Transistor Circuits Budget-Friendly Options

6. Navigating Analysis And Design Of Transistor Circuits eBook Formats
 - ePub, PDF, MOBI, and More
 - Analysis And Design Of Transistor Circuits Compatibility with Devices
 - Analysis And Design Of Transistor Circuits Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Analysis And Design Of Transistor Circuits
 - Highlighting and Note-Taking Analysis And Design Of Transistor Circuits
 - Interactive Elements Analysis And Design Of Transistor Circuits
8. Staying Engaged with Analysis And Design Of Transistor Circuits
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Analysis And Design Of Transistor Circuits
9. Balancing eBooks and Physical Books Analysis And Design Of Transistor Circuits
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Analysis And Design Of Transistor Circuits
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Analysis And Design Of Transistor Circuits
 - Setting Reading Goals Analysis And Design Of Transistor Circuits
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Analysis And Design Of Transistor Circuits
 - Fact-Checking eBook Content of Analysis And Design Of Transistor Circuits
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements

- Interactive and Gamified eBooks

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