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Circuits and
Technology
Meeting, 1999**

IEEE

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operate electronics in the most demanding conditions *Index to IEEE Publications* Institute of Electrical and Electronics Engineers,1998 Issues for 1973 cover the entire IEEE technical literature **Silicon Heterostructure Devices** John D. Cressler,2018-10-03 SiGe HBTs are the most mature of the Si heterostructure devices and not surprisingly the most completely researched and discussed in the technical literature However new effects and nuances of device operation are uncovered year after year as transistor scaling advances and application targets march steadily upward in frequency and sophistication Providing a comprehensive treatment of SiGe HBTs *Silicon Heterostructure Devices* covers an amazingly diverse set of topics ranging from basic transistor physics to noise radiation effects reliability and TCAD simulation Drawn from the comprehensive and well reviewed *Silicon Heterostructure Handbook* this text explores SiGe heterojunction bipolar transistors HBTs heterostructure FETs various other heterostructure devices as well as optoelectronic components The book provides an overview characteristics and derivative applications for each device covered It discusses device physics broadband noise performance limits reliability engineered substrates and self assembling nanostructures Coverage of optoelectronic devices includes Si SiGe LEDs near infrared detectors photonic transistors for integrated optoelectronics and quantum cascade emitters In addition to this substantial collection of material the book concludes with a look at the ultimate limits of SiGe HBTs scaling It contains easy to reference appendices on topics including the properties of silicon and germanium the generalized Moll Ross relations and the integral charge control model and sample SiGe HBT compact model parameters *Silicon Heterostructure Handbook* John D. Cressler,2018-10-03 An extraordinary combination of material science manufacturing processes and innovative thinking spurred the development of SiGe heterojunction devices that offer a wide array of functions unprecedented levels of performance and low manufacturing costs While there are many books on specific aspects of Si heterostructures the *Silicon Heterostructure Handbook Materials Fabrication Devices Circuits and Applications of SiGe and Si Strained Layer Epitaxy* is the first book to bring all aspects together in a single source Featuring broad comprehensive and in depth discussion this handbook distills the current state of the field in areas ranging from materials to fabrication devices CAD circuits and applications The editor includes snapshots of the industrial state of the art for devices and circuits presenting a novel perspective for comparing the present status with future directions in the field With each chapter contributed by expert authors from leading industrial and research institutions worldwide the book is unequalled not only in breadth of scope but also in depth of coverage timeliness of results and authority of references It also includes a foreword by Dr Bernard S Meyerson a pioneer in SiGe technology Containing nearly 1000 figures along with valuable appendices the *Silicon Heterostructure Handbook* authoritatively surveys materials fabrication device physics transistor optimization optoelectronics components measurement compact modeling circuit design and device simulation

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