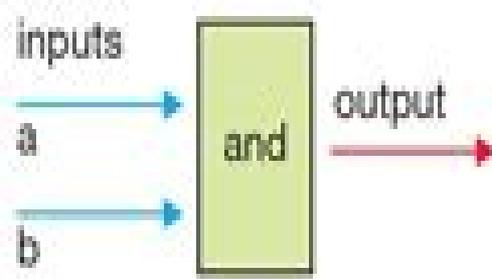


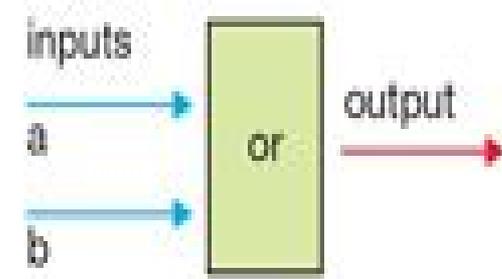
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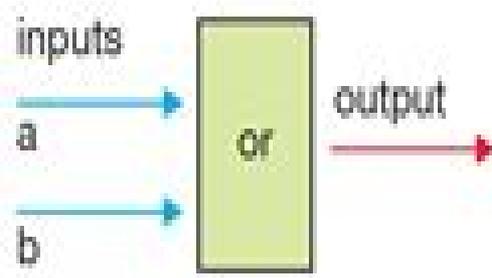
inputs		output
a	b	
0	0	0
0	1	0
1	0	0
1	1	1

EXCLUSIVE OR



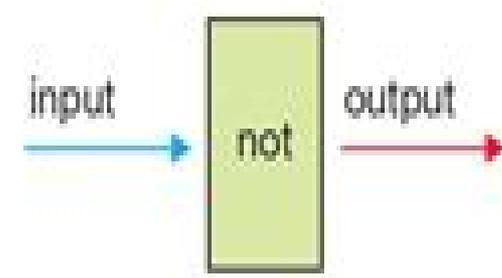
inputs		output
a	b	
0	0	0
0	1	1
1	0	1
1	1	0

OR



inputs		output
a	b	
0	0	0
0	1	1
1	0	1
1	1	1

NOT



input	output
0	1
1	0

Digital Logic Operation And Analysis

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