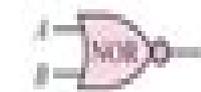
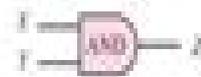


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Each is made of about a dozen resistors, transistors, and diodes, but we'll treat them as black boxes:



Logical Operation	statement
OR	If either X or Y is true (1), then Z is true (1)
AND	If both X and Y are true (1), then Z is true (1)
NOT (inverter)	Provides the complement of the logic variable connected to its input. If input is 1, output is 0. If input is 0, output is 1.
NAND	Corresponds to an AND gate followed by an inverter.
NOR	Corresponds to an OR gate followed by an inverter.
XOR	Exclusive OR. Acts as an OR except when all of its inputs are 1s. If all inputs are 1s, returns a logic 0.

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web er betrieb intensive wissenschaftliche studien und unternahm reisen in das rheinland elsaß nach lothringen und obersachsen 1777 erhielt er den titel eines weimarerer

knigge die biografie die biographie ab 0 91 - Apr 16 2022

web jun 27 2022 interview mit freiherrn von knigge und nicole rösler begeistert arbeiten über sein leben und sein werk