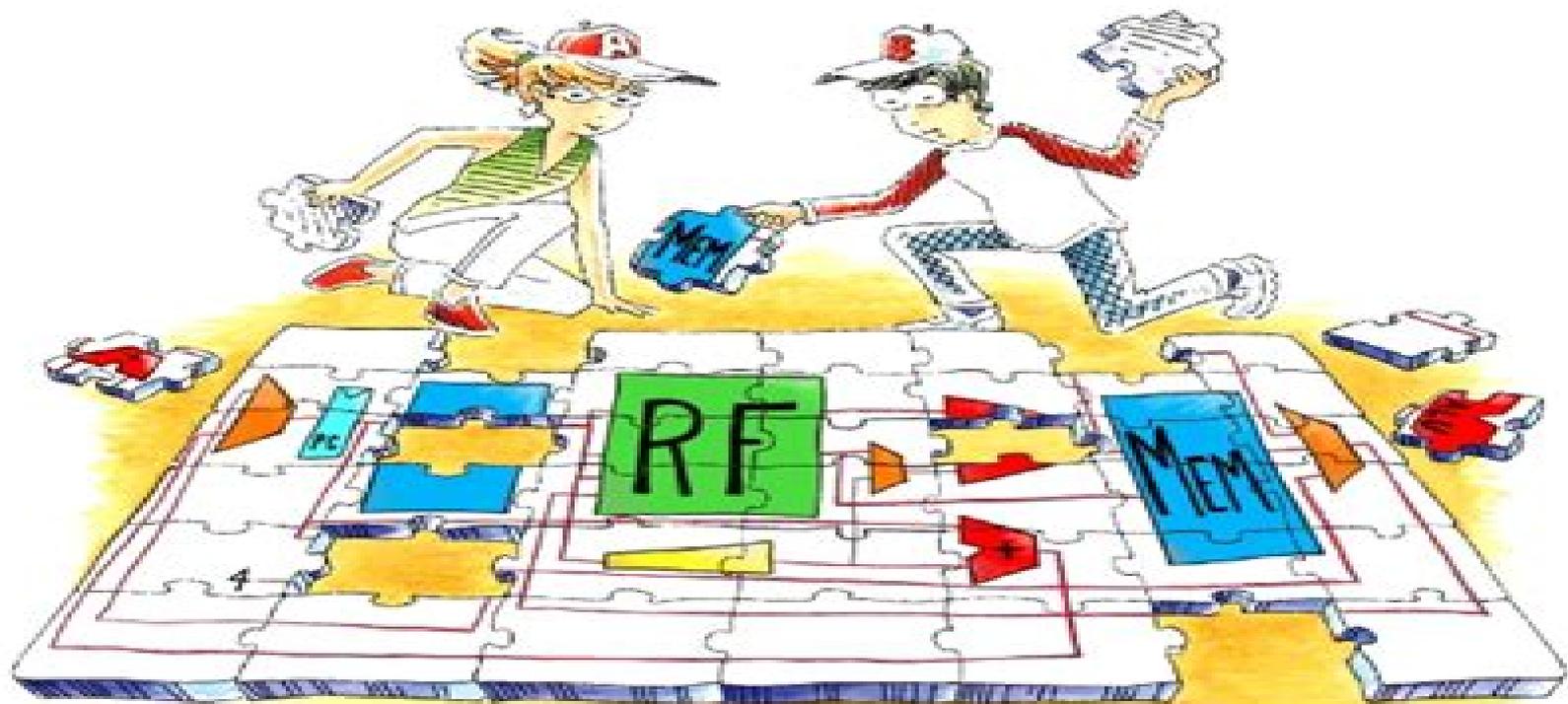


# Digital Design and Computer Architecture

## RISC-V Edition



**MK**  
MORGAN KAUFMANN

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# Digital Design Principles And Computer Architecture

**Leonard R. Marino**



## **Digital Design Principles And Computer Architecture:**

**Digital Design Principles and Computer Architecture** Edward Karalis, 1997 This text articulates the underlying principles and practices that comprise today's digital components and systems. It provides a comprehensive yet concise treatment of relevant analytical tools and design methodologies as applied to various integrated devices, modules, systems, and signal interactions. Coverage offers a balance of theory, application, and functional level examples. The text integrates over 100 examples and over 170 problems to reinforce and expand basic concepts, and over 100 tables and 300 figures to allow quick access to reference data. Visual representations of main topics are also included. Over 100 equations mathematically define key concepts.

**Digital Design Principles and Computer Architecture** KARALIS, 1996-07-01 **Digital Design Principles and Computer Architecture** Edward Karalis, 1997 Appropriate for courses in Digital Design, Microprocessor Based Design, Computer Architecture, Computer Communications and Networks, and Analog and Mixed Signal Design, found in 2-year and 4-year electronics technology and engineering departments. Based on the author's 20 years of hands-on experience, this text articulates the underlying principles and practices that comprise today's digital components and systems. Karalis provides a comprehensive yet concise, fully integrated treatment of relevant analytical tools and design methodologies as applied to various integrated devices, modules, systems, and signal interactions. Coverage offers a balance of theory, application, and functional level examples.

Digital Design and Computer Architecture, ARM Edition Sarah Harris, David Harris, 2015-04-09 **Digital Design and Computer Architecture ARM Edition** covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor. By the end of this book, readers will be able to build their own microprocessor and will have a top-to-bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing an ARM processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. The companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs, Bluetooth radios, and motors. This book will be a valuable resource for students taking a course that combines digital logic and computer architecture, or students taking a two-quarter sequence in digital logic and computer organization/architecture. Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor. Features side-by-side examples of the two most prominent Hardware Description Languages (HDLs), SystemVerilog and VHDL, which illustrate and compare the ways each can be used in the design of digital systems. Includes examples throughout the text that enhance the reader's understanding and retention of key concepts and techniques. The Companion website includes a chapter on I/O systems with

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an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of a processor By the end of this book readers will be able to build their own RISC V microprocessor and will have a top to bottom understanding of how it works Beginning with digital logic gates and progressing to the design of combinational and sequential circuits this book uses these fundamental building blocks as the basis for designing a RISC V processor SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design The companion website includes a chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Gives students a full understanding of the RISC V instruction set architecture enabling them to build a RISC V processor and program the RISC V processor in hardware simulation software simulation and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single cycle multicycle and pipelined versions of the RISC V architecture Features a companion website with a bonus chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive problems

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independent of each other chapters To facilitate the assimilation and practical implementation of various concepts the book is complemented by a selection of practical exercises *Computer Organization and Design Fundamentals* David L. Tarnoff,2007 Computer Organization and Design Fundamentals takes the reader from the basic design principles of the modern digital computer to a top level examination of its architecture This book can serve either as a textbook to an introductory course on computer hardware or as the basic text for the aspiring geek who wants to learn about digital design The material is presented in four parts The first part describes how computers represent and manipulate numbers The second part presents the tools used at all levels of binary design The third part introduces the reader to computer system theory with topics such as memory caches hard drives pipelining and interrupts The last part applies these theories through an introduction to the Intel 80x86 architecture and assembly language The material is presented using practical terms and examples with an aim toward providing anyone who works with computer systems the ability to use them more effectively through a better understanding of their design

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