Advanced Techniques for Embedded Systems Design and Test

Edited by

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Advanced Techniques For Embedded Systems Design And Test:

Advanced Techniques for Embedded Systems Design and Test Juan C. López,Román Hermida,Walter Geisselhardt,2013-03-09 As electronic technology reaches the point where complex systems can be integrated on a single chip and higher degrees of performance can be achieved at lower costs designers must devise new ways to undertake the laborious task of coping with the numerous and non trivial problems that arise during the conception of such systems On the other hand shorter design cycles so that electronic products can fit into shrinking market windows put companies and consequently designers under pressure in a race to obtain reliable products in the minimum period of time New methodologies supported by automation and abstraction have appeared which have been crucial in making it possible for system designers to take over the traditional electronic design process and embedded systems is one of the fields that these methodologies are mainly targeting The inherent complexity of these systems with hardware and software components that usually execute concurrently and the very tight cost and performance constraints make them specially suitable to introduce higher levels of abstraction and automation so as to allow the designer to better tackle the many problems that appear during their design Advanced Techniques for Embedded Systems Design and Test is a comprehensive book presenting recent developments in methodologies and tools for the specification synthesis verification and test of embedded systems characterized by the use of high level languages as a road to productivity Each specific part of the design process from specification through to test is looked at with a constant emphasis on behavioral methodologies Advanced Techniques for Embedded Systems Design and Test is essential reading for all researchers in the design and test communities as well as system designers and CAD tools developers **Design of Dependable Computing Systems** J.C. Geffroy,G. Motet,2013-03-09 This book analyzes the causes of failures in computing systems their consequences as well as the existing solutions to manage them The domain is tackled in a progressive and educational manner with two objectives 1 The mastering of the basics of dependability domain at system level that is to say independently ofthe technology used hardware or software and of the domain of application 2 The understanding of the fundamental techniques available to prevent to remove to tolerate and to forecast faults in hardware and software technologies The first objective leads to the presentation of the general problem the fault models and degradation mechanisms wh ich are at the origin of the failures and finally the methods and techniques which permit the faults to be prevented removed or tolerated This study concerns logical systems in general independently of the hardware and software technologies put in place This knowledge is indispensable for two reasons A large part of a product s development is independent of the technological means expression of requirements specification and most of the design stage Very often the development team does not possess this basic knowledge hence the dependability requirements are considered uniquely during the technological implementation Such an approach is expensive and inefficient Indeed the removal of a preliminary design fault can be very difficult if possible if this fault is detected during

the product s final testing **SystemC** Wolfgang Müller,Wolfgang Rosenstiel,Jürgen Ruf,2007-05-08 SystemC has received a wide acceptance by users and tool vendors as the next generation system description language in order to deal with higher levels of abstraction for complex SoC designs SystemC Methodologies and Applications gives a comprehensive survey on the state of the art of SystemC in industry and research Organised into 11 self contained chapters selected SystemC experts present their approaches in the domains of modelling analysis synthesis Their contributions range from mixed signal and discrete system to embedded software The chapters give a broad overview of recent advances in SystemC methodologies and applications and are mainly based on presentations given at the European SystemC User Group meetings *14th Symposium on Integrated Circuits and Systems Design* Sociedade Brasileira de Computação,2001 Annotation Papers from a September 2001 symposium report on recent advances in areas of integrated circuits and systems design including embedded systems rapid prototyping formal methods codesign CAD and test analog digital and physical design and low power and low voltage Specific topics include communication architectures for system on chip using the CAN protocol and reconfigurable computing technology for Web based smart house automation and optimizing BBD based verification analyzing variable dependencies Other subjects include interconnection length estimation at logic level an environment to aid the synthesis of threephase analogue waveform using AHDL and extending sequencing graphs for reconfigurable applications modeling This work lacks a subject index c Book News Inc <u>Innovations and Advanced Techniques in Computer and Information Sciences and Engineering</u> Tarek Sobh,2007-09-04 Innovations and Advanced Techniques in Computer and Information Sciences and Engineering includes a set of rigorously reviewed world class manuscripts addressing and detailing state of the art research projects in the areas of Computer Science Computer Engineering and Information Sciences Innovations and Advanced Techniques in Computer and Information Sciences and Engineering includes selected papers form the conference proceedings of the International Conference on Systems Computing Sciences and Software Engineering SCSS 2006 which was part of the International Joint Conferences on Computer Information and Systems Sciences and Engineering CISSE 2006 All aspects of the conference were managed on line not only the reviewing submissions and registration processes but also the actual conference Conference participants authors presenters and attendees only needed an internet connection and sound available on their computers in order to be able to contribute and participate in this international ground breaking conference The on line structure of this high quality event allowed academic professionals and industry participants to contribute work and attend world class technical presentations based on rigorously refereed submissions live without the need for investing significant travel funds or time out of the office Suffice to say that CISSE received submissions from more than 70 countries for whose researchers this opportunity presented a much more affordable dynamic and well planned event to attend and submit their work to versus a classic on the ground conference The CISSE conference audio room provided superb audio even over low speed internet connections the ability to display

PowerPoint presentations and cross platform compatibility the conferencing software runs on Windows Mac and any other operating system that supports Java In addition the conferencing system allowed for an unlimited number of participants which in turn granted CISSE the opportunity to allow all participants to attend all presentations as opposed to limiting the number of available seats for each session   International Workshop on Electronic Design, Test and Applications Michel Renovell,2002 A collection of the 78 oral presentations and 24 poster papers from the January 2002 international workshop which brought together specialists from a broad area of electronic design manufacturing test and advanced system applications in the hope that the conference would integrate design test and application as cross dependent disciplines The contributions are organized into sessions focusing on analog test communications digital signal processing and architectures low to high level fault simulation and identification high level design memory power issues in design and test sensor and analog design electrical engineering education electromagnetics and control fault tolerant digital systems image processing robotics submicron technology test generation and compaction and test techniques and methodologies Annotation copyrighted by Book News Inc Portland OR   **Design and Test Technology for Dependable Systems-on-Chip** Ubar, Raimund,Raik, Jaan,Vierhaus, Heinrich Theodor,2010-12-31 This book covers aspects of system design and efficient modelling and also introduces various fault models and fault mechanisms associated with digital circuits integrated into System on Chip SoC Multi Processor System on Chip MPSoC or Network on Chip NoC   **Electronic Chips & Systems Design Languages** Jean Mermet,2001-03-31 Electronic Chips Systems Design Languagesoutlines and describes the latest advances in design languages The challenge of System on a Chip SOC design requires designers to work in a multi lingual environment which is becoming increasingly difficult to master It is therefore crucial for them to learn almost in real time from the experiences of their colleagues in the use of design languages and how these languages have become more advanced to cope with system design System designers as well as students willing to become system designers often do not have the time to attend all scientific events where they could learn the necessary information This book will bring them a selected digest of the best contributions and industry strength case studies All the levels of abstraction that are relevant from the informal user requirements down to the implementation specifications are addressed by different contributors The author together with colleague authors who provide valuable additional experience presents examples of actual industrial world applications Furthermore the academic concepts presented in this book provide excellent theories to student readers and the concepts described are up to date and in so doing provide most suitable root information for Ph D postgraduates

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