

Advanced Logical Circuit Design Technique

Svoboda, Antonin

Advanced Logical Circuit Design Techniques

Milos D. Ercegovic, Tomás Lang



Advanced Logical Circuit Design Techniques:

Advanced Logical Circuit Design Techniques Antonín Svoboda,Donnamaie E. White,1979 Advanced Logical Circuit Design Techniques Donnamaie E. White,2000 Logic Minimization Algorithms for VLSI Synthesis Robert K. Brayton,Gary D. Hachtel,C. McMullen,Alberto L. Sangiovanni-Vincentelli,2012-12-06 The roots of the project which culminates with the writing of this book can be traced to the work on logic synthesis started in 1979 at the IBM Watson Research Center and at University of California Berkeley During the preliminary phases of these projects the importance of logic minimization for the synthesis of area and performance effective circuits clearly emerged In 1980 Richard Newton stirred our interest by pointing out new heuristic algorithms for two level logic minimization and the potential for improving upon existing approaches In the summer of 1981 the authors organized and participated in a seminar on logic manipulation at IBM Research One of the goals of the seminar was to study the literature on logic minimization and to look at heuristic algorithms from a fundamental and comparative point of view The fruits of this investigation were surprisingly abundant it was apparent from an initial implementation of recursive logic minimization ESPRESSO I that if we merged our new results into a two level minimization program an important step forward in automatic logic synthesis could result ESPRESSO II was born and an APL implementation was created in the summer of 1982 The results of preliminary tests on a fairly large set of industrial examples were good enough to justify the publication of our algorithms It is hoped that the strength and speed of our minimizer warrant its Italian name which denotes both express delivery and a specially brewed black coffee

Introduction to Logic Design Svetlana N. Yanushkevich,Vlad P. Shmerko,2008-01-25 With an abundance of insightful examples problems and computer experiments Introduction to Logic Design provides a balanced easy to read treatment of the fundamental theory of logic functions and applications to the design of digital devices and systems Requiring no prior knowledge of electrical circuits or electronics it supplies the Modern VLSI Design Wayne Wolf,2008-12-21 The Number 1 VLSI Design Guide Now Fully Updated for IP Based Design and the Newest Technologies Modern VLSI Design Fourth Edition offers authoritative up to the minute guidance for the entire VLSI design process from architecture and logic design through layout and packaging Wayne Wolf has systematically updated his award winning book for today s newest technologies and highest value design techniques Wolf introduces powerful new IP based design techniques at all three levels gates subsystems and architecture He presents deeper coverage of logic design fundamentals clocking and timing and much more No other VLSI guide presents as much up to date information for maximizing performance minimizing power utilization and achieving rapid design turnarounds **Switching Theory for Logic Synthesis** Tsutomu Sasao,2012-12-06 Switching Theory for Logic Synthesis covers the basic topics of switching theory and logic synthesis in fourteen chapters Chapters 1 through 5 provide the mathematical foundation Chapters 6 through 8 include an introduction to sequential circuits optimization of sequential machines and asynchronous sequential circuits Chapters 9 through 14 are the main feature of the

book These chapters introduce and explain various topics that make up the subject of logic synthesis multi valued input two valued output function logic design for PLDs FPGAs EXOR based design and complexity theories of logic networks An appendix providing a history of switching theory is included The reference list consists of over four hundred entries Switching Theory for Logic Synthesis is based on the author s lectures at Kyushu Institute of Technology as well as seminars for CAD engineers from various Japanese technology companies Switching Theory for Logic Synthesis will be of interest to CAD professionals and students at the advanced level It is also useful as a textbook as each chapter contains examples illustrations and exercises

Logic Synthesis and Verification Soha Hassoun,Tsutomu Sasao,2001-11-30 Research and development of logic synthesis and verification have matured considerably over the past two decades Many commercial products are available and they have been critical in harnessing advances in fabrication technology to produce today s plethora of electronic components While this maturity is assuring the advances in fabrication continue to seemingly present unwieldy challenges Logic Synthesis and Verification provides a state of the art view of logic synthesis and verification It consists of fifteen chapters each focusing on a distinct aspect Each chapter presents key developments outlines future challenges and lists essential references Two unique features of this book are technical strength and comprehensiveness The book chapters are written by twenty eight recognized leaders in the field and reviewed by equally qualified experts The topics collectively span the field Logic Synthesis and Verification fills a current gap in the existing CAD literature Each chapter contains essential information to study a topic at a great depth and to understand further developments in the field The book is intended for seniors graduate students researchers and developers of related Computer Aided Design CAD tools

From the foreword The commercial success of logic synthesis and verification is due in large part to the ideas of many of the authors of this book Their innovative work contributed to design automation tools that permanently changed the course of electronic design by Aart J de Geus Chairman and CEO Synopsys Inc

VLSI Fault Modeling and Testing Techniques George W. Zobrist,1993 VLSI systems are becoming very complex and difficult to test Traditional stuck at fault problems may be inadequate to model possible manufacturing defects in the integrated circuit Hierarchical models are needed that are easy to use at the transistor and functional levels Stuck open faults present severe testing problems in CMOS circuits to overcome testing problems testable designs are utilized Bridging faults are important due to the shrinking geometry of ICs BIST PLA schemes have common features controllability and observability which are enhanced through additional logic and test points Certain circuit topologies are more easily testable than others The amount of reconvergent fan out is a critical factor in determining realistic measures for determining test generation difficulty Test implementation is usually left until after the VLSI data path has been synthesized into a structural description This leads to investigation methodologies for performing design synthesis with test incorporation These topics and more are discussed

Czechs Won't Get Lost in the World, Let Alone in America Miloslav Rechcigl Jr.,2018-05-02 This book features a panorama of the lives of selected personalities whose

roots had origin in the Czech lands and who in the US reached extraordinary success and who with their activities substantially influenced the growth and development of their new homeland It is a saga of plain as well as powerful people whose influence and importance often exceeded the borders of the US A great portion of included individuals may be unknown to readers since it concerns persons whose Czech origin was usually not known The book covers the total period from the times of the discovery of New World to the end of the twentieth century During the selection little concern was given to nationalistic or ethnographic criteria the only prerequisite was that the respected individuals were either born on the territory of the Czech lands or were descendants of emigrants from the Czech lands The image on the front cover is a portrait of Augustine Herman Lord of Bohemia Manor the first documented Czech immigrant in the United States The portrait comes from his famous Map of Maryland and Virginia dated 1670 The colorful story of his life would be unbelievable if made into a movie Pioneer merchant explorer surveyor map maker patriot rebel diplomat and finally Lord Read more about him in the book USITC Publication ,1998 Electronics ,1945-07 June issues 1941 44 and Nov issue 1945 include a buyers guide section *Learning Resources* ,1977 **Selected Papers on Logic Synthesis for Integrated Circuit Design** Arthur Richard Newton,1987 **Standard Handbook of Engineering Calculations** Tyler Gregory Hicks,1995 This invaluable handbook provides engineers and technicians with more than 5 000 direct and related calculations for solving day to day problems quickly and easily The book covers 13 disciplines including civil architectural mechanical electrical electronics and nuclear engineering enabling readers to become familiar with procedures in fields apart from their own *Proceedings of the 1984 Custom Integrated Circuits Conference, Genesee Plaza/Holiday Inn, Rochester, NY, May 21-23, 1984* ,1984 **Digital Systems and Hardware/Firmware Algorithms** Milos D. Ercegovic,Tomás Lang,1985-05-14 This modern treatment of digital system specification analysis and design covers all topics from gates and flip flops to complex hardware and system software algorithms An upper level undergraduate graduate text it uses two complementary approaches system model and algorithmic model in dealing with structured analysis and design and separates specification from implementation to allow for the ready application of concepts to practical system design Extensive illustrations and 500 exercises **Annual Workshop on Interactive Computing : Computer-aided Education & Electrical Engineering Education** Workshop on Interactive Computing,1984 *Warp Processing and Just-in-time Field-programmable Gate Array Compilation* Roman Lev Lysecky,2005 *Introduction to Computer Organization* Ivan Tomek,1981 *Advances in Electronic Circuit Packaging* ,1963

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