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Gérard Blanchet, Bertrand Dupouy



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Computer Architecture Gérard Blanchet,Bertrand Dupouy,2013-01-24 This book lays out the concepts necessary to understand how a computer works For reasons of clarity the authors have deliberately chosen examples that apply to machines from all eras without having to water down the contents of the book This choice helps to show how techniques concepts and performances have evolved since the first computers The book is divided into five parts The first four which are of increasing difficulty are the core of the book Elements of a Basic Architecture Programming Model and Operation Memory Hierarchy Parallelism and Performance Enhancement The final part provides hints and solutions to the exercises in the book as well as appendices The reader may approach each part independently based on their prior knowledge and goals

Advanced Computer Architecture, 2E Kai Hwang,2011

Advanced Computer Architecture Kai Hwang,1993 This text presents the latest technologies for parallel processing and high performance computing It deals with advanced computer architecture and parallel processing systems and techniques providing an integrated study of computer hardware and software systems and the material is suitable for use on courses found in computer science computer engineering or electrical engineering departments

Computer Architecture John L. Hennessy,David A. Patterson,Christos Kozyrakis,2025-12-02 Computer Architecture A Quantitative Approach has been considered essential reading by instructors students and practitioners of computer design for nearly 30 years The seventh edition of this classic textbook from John Hennessy and David Patterson w Winner of a 2019 Textbook Excellence Award Texty from the Textbook and Academic Authors Association Each chapter follows a consistent framework explanation of the ideas in each chapter a crosscutting issues section which presents how the concepts covered in one chapter connect with those given in other chapters a putting it all together section that links these concepts by discussing how they are applied in real machine and detailed examples of misunderstandings and architectural traps commonly encountered by developers and architects Includes Putting It All Together sections near the end of every chapter providing real world technology examples that demonstrate the principles covered in each chapter Covers new developments in GPU

and CPU architectures as well as domain specific architectures Features more comprehensive coverage of systems on chip and heterogeneity

Computer Architecture: A Minimalist Perspective William F. Gilreath, Phillip A.

Laplante, 2003-03-31 This book examines computer architecture computability theory and the history of computers from the perspective of minimalist computing a framework in which the instruction set consists of a single instruction This approach is different than that taken in any other computer architecture text and it is a bold step The audience for this book is researchers computer hardware engineers software engineers and systems engineers who are looking for a fresh unique perspective on computer architecture Upper division undergraduate students and early graduate students studying computer architecture computer organization or embedded systems will also find this book useful A typical course title might be Special Topics in Computer Architecture The organization of the book is as follows First the reasons for studying such an esoteric subject are given Then the history and evolution of instruction sets is studied with an emphasis on how modern computing has features of one instruction computing Also previous computer systems are reviewed to show how their features relate to one instruction computers Next the primary forms of one instruction set computing are examined The theories of computation and of Turing machines are also reviewed to examine the theoretical nature of one instruction computers Other processor architectures and instruction sets are then mapped into single instructions to illustrate the features of both types of one instruction computers In doing so the features of the processor being mapped are highlighted

Computer Architecture John L. Hennessy, David A. Patterson, Krste Asanović, 2012 The computing world is in the middle of a revolution mobile clients and cloud computing have emerged as the dominant paradigms driving programming and hardware innovation This book focuses on the shift exploring the ways in which software and technology in the cloud are accessed by cell phones tablets laptops and more

Computer Architecture Caxton C. Foster, 1976 The representation of information Gates and elementary logic Storage mechanisms Putting the bits together An elementary machine Variation in addressing Variations in input output Other instructions The micro computer Very large computers Parallelism and distributed logic Tessellated computers

Computer Organization V. Carl Hamacher, Zvonko G. Vranesic, Safwat G. Zaky, 1996 This text is intended to be of use for first courses in computer architecture taught in computer science and electrical computing engineering departments The fourth edition places more emphasis on design and covers topics such as RISC processors performance analysis and memory systems

Computer Architecture and Organization John P. Hayes, 1982 *Computer Architecture and Organization* Miles J. Murdocca, Vincent P. Heuring, 2007-03-16 An accessible introduction to computer systems and architecture Anyone aspiring to more advanced studies in computer related fields must gain an understanding of the two parallel aspects of the modern digital computer programming methodology and the underlying machine architecture The uniquely integrated approach of *Computer Architecture and Organization* connects the programmer's view of a computer system with the associated hardware and peripheral devices providing a thorough three

dimensional view of what goes on inside the machine Covering all the major topics normally found in a first course in computer architecture the text focuses on the essentials including the instruction set architecture ISA network related issues and programming methodology Using real world case studies to put the information into perspective the chapters examine Data representation Arithmetic The instruction set architecture Datapath and Control Languages and the machine Memory Buses and peripherals Networking and communication Advanced computer architecture A valuable feature of this book is the use of ARC a subset of the SPARC processor for an instruction set architecture A platform independent ARCTools suite containing an assembler and simulator for the ARC ISA that supports the examples used in the book is available Better yet the content is supplemented by online problem sets available through WileyPlus Balanced and thoughtfully designed for use as either a classroom text or self study guide Computer Architecture and Organization An Integrated Approach will put you solidly on track for advancing to higher levels in computer related disciplines About the Author MILES MURDOCCA serves as the President and CEO of Internet Institute USA IIUSA a private postsecondary information technology IT school specializing in networking operating systems IP telephony programming and security Previously Dr Murdocca has been a computer science faculty member at Rutgers University and a research scientist at AT T Bell Laboratories working in computer architecture networking and digital optical computing He is the author of A Digital Design Methodology for Optical Computing and Principles of Computer Architecture and a contributing author to Computer Systems Design and Architecture Second Edition as well as the author of dozens of professional papers and patents relating to information technology VINCE HEURING is an associate professor and acting chair of the Department of Electrical and Computer Engineering at the University of Colorado at Boulder He has been at the university since 1984 and prior to that he spent three years at the University of Cincinnati Professor Heuring s research encompasses computer architectures and programming language design implementation He and his colleague Harry Jordan designed and built the world s first stored program optical computer SPOC

Computer Architecture Michael J. Flynn, 1995 *Computer Architecture Software Engineering*

Intelligent Internet Knowledge Networks Syed V. Ahamed, 2006-12-13 Introducing the basic concepts in total program control of the intelligent agents and machines Intelligent Internet Knowledge Networks explores the design and architecture of information systems that include and emphasize the interactive role of modern computer communication systems and human beings Here you ll discover specific network configurations that sense environments presented through case studies of IT platforms electrical governments medical networks and educational networks

Computer Architecture and Communications Neil Willis, 1993-01-01 **Architecture of High Performance Computers** Roland N. Ibbett, 1989 *Computer Architecture* John L. Hennessy, David A. Patterson, 2016-02 **Mathematical Foundations of Computer Science 1986** Jozef Gruska, Branislav Rován, Juraj Wiedermann, 1986-08-01 **Advanced Computer Architectures** Desző Sima, Péter Kacsuk, 1997 This timely book provides an unconventional and up to date overview of all the important computer

architectures and is one of the first texts to present all the relevant concepts of advanced architecture classes by exploring their design spaces Advanced Computer Architectures will prove an indispensable guide for anyone who needs to be acquainted with the relevant concepts and solutions introduced in recent years to the dramatically changing world of computer architecture For the student of advanced level courses in computer architecture this book will provide a comprehensive and accessible overview of the subject whilst its strong practical orientation will make it an invaluable reference for the practitioner Features Explores design spaces for each architecture class and exposes evolution of concepts and design issues Provides an up to date overview of significant architecture classes including unique in depth coverage of superscalar architectures as well as multithreaded shared and distributed memory MIMDs and associative and neural architectures Identifies which concepts and design choices have been made use of in important processors and illustrates significant trends and surpassed and viable concepts Case studies and tables show microarchitectural details of relevant processors including the PentiumPro PowerPC 604 PowerPC 620 and R10000 allowing comparisons between them

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