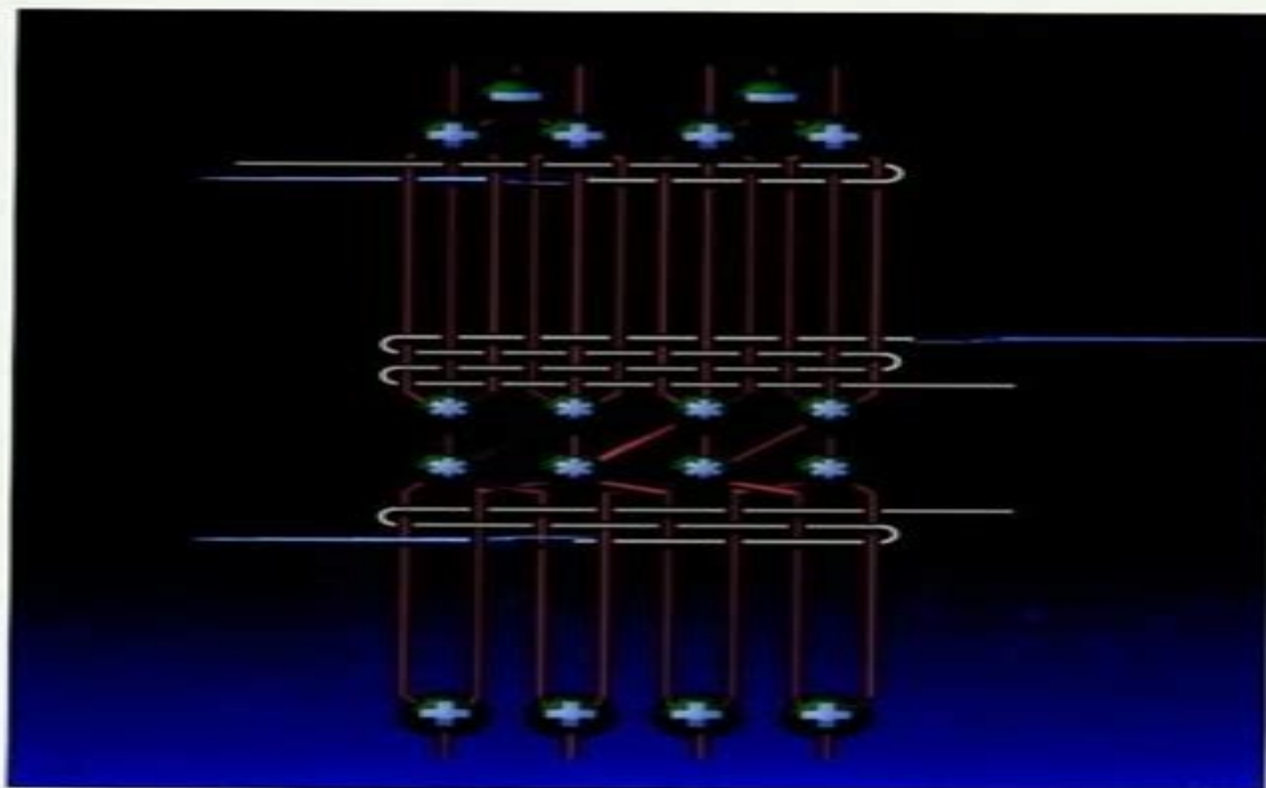


# ADVANCED TOPICS IN DATAFLOW COMPUTING AND MULTITHREADING

*Guang R. Gao, Lubomir Bic, and Jean-Luc Gaudiot*



# Advanced Topics In Dataflow Computing And Multithreading

**S Baum**



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James Brodman, Peng Tu, 2015-04-30 This book constitutes the thoroughly refereed post conference proceedings of the 27th International Workshop on Languages and Compilers for Parallel Computing LCPC 2014 held in Hillsboro OR USA in September 2014 The 25 revised full papers were carefully reviewed and selected from 39 submissions The papers are organized in topical sections on accelerator programming algorithms for parallelism compilers debugging vectorization

High Performance Computing Alex Veidenbaum, 2003-10-09 This book constitutes the refereed proceedings of the 5th International Symposium on High Performance Computing ISHPC 2003 held in Tokyo Odaiba Japan in October 2003 The 23 revised full papers and 16 short papers presented together with 4 invited papers and 7 refereed papers accepted for a concurrently held workshop on OpenMP WOMPEI 2003 were carefully reviewed and selected from 58 submissions The papers are organized in topical sections on architecture software applications and ITBL **Computer Architecture** Joseph D. Dumas II, 2018-10-03 Future computing professionals must become familiar with historical computer architectures because many of the same or similar techniques are still being used and may persist well into the future *Computer Architecture Fundamentals and Principles of Computer Design* discusses the fundamental principles of computer design and performance enhancement that have proven effective and demonstrates how current trends in architecture and implementation rely on these principles while expanding upon them or applying them in new ways Rather than focusing on a particular type of machine this textbook explains concepts and techniques via examples drawn from various architectures and implementations When necessary the author creates simplified examples that clearly explain architectural and implementation features used across many computing platforms Following an introduction that discusses the difference between architecture and implementation and how they relate the next four chapters cover the architecture of traditional single processor systems that are still after 60 years the most widely used computing machines The final two chapters explore approaches to adopt when single processor systems do not reach desired levels of performance or are not suited for intended applications Topics include parallel systems major classifications of architectures and characteristics of unconventional systems of the past present and future This textbook provides students with a thorough grounding in what constitutes high performance and how to measure it as well as a full familiarity in the fundamentals needed to make systems perform better This knowledge enables them to understand and evaluate the many new systems they will encounter

throughout their professional careers

**Readings in Hardware/Software Co-Design** Giovanni De Micheli, Rolf Ernst, Wayne Wolf, 2001-06-19 Embedded system designers are constantly looking for new tools and techniques to help satisfy the exploding demand for consumer information appliances and specialized industrial products One critical barrier to the timely release of embedded system products is integrating the design of the hardware and software systems Hardware software co design is a set of methodologies and techniques specifically created to support the concurrent design of both systems effectively reducing multiple iterations and major redesigns In addition to its critical role in the development of embedded systems many experts believe that co design will be a key design methodology for Systems on a Chip Readings in Hardware Software Co Design presents the papers that have shaped the hardware software co design field since its inception in the early 90s Field experts Giovanni De Micheli Rolf Ernst and Wayne Wolf introduce sections of the book and provide context for the paper that follow This collection provides professionals researchers and graduate students with a single reference source for this critical aspect of computing design Over 50 peer reviewed papers written from leading researchers and designers in the field Selected edited and introduced by three of the fields most eminent researchers and educators Accompanied by an annually updated companion Web site with links and references to recently published papers providing a forum for the editors to comment on how recent work continues or breaks with previous work in the field

**Parallel Architectures and Compilation Techniques**, 1997 **High Performance Computing**, 2003 Annual ACM Symposium on Parallel Algorithms and Architectures, 1997 Research Directions in Parallel Functional Programming Kevin Hammond, 1999-11 This volume provides a state of the art survey of research trends in parallel functional programming The text is divided into two sections the first section gives comprehensive introductions to key issues such as foundations programming constructs proof architectures and implementations the second comprises shorter summaries of research areas which are either of particular interest at the moment or which promise to provide key developments in the near future Topics covered here include coordination languages performance monitoring data flow programming explicit parallelism BSP and cost modelling Contributions have been commissioned by key researchers and practitioners in the area including several from the US and Canada where this is an area of increasing interest Research Directions in Parallel Functional Programming will be of interest to researchers post graduate students and practitioners in all relevant areas

*Parallel and Distributed Processing*, 1999 *High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Algorithms and Architectures* K. J. Ray Liu, Kung Yao, 1998 Electrical Engineering Signal Processing High Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 1 Algorithms and Architectures The first volume in a two volume set High Performance VLSI Signal Processing Innovative Architectures and Algorithms brings together the most innovative papers in the field focused introductory material and extensive references The editors present timely coverage of algorithm and design methodologies with an emphasis on today s rapidly evolving high speed

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**Progress in Computer Research** Frank H. Columbus,2001 Computer designers programmers and other specialists present 14 papers addressing a range of topics including parallel programming using visual patterns visualization of do loop performance using AVS an application framework for dynamic distributed software architectures parallel algorithms for multi dimensional integration and a new fast message passing communication system for multiprocessor workstation clusters Almost all the papers include an abstract list key words and include an introduction extensive discussion of the research conclusions suggestions for future work references and useful charts figures and diagrams c Book News Inc

**Foundations of Parallel Programming** Theodore Gyle Lewis,1993 Mathematics of Computing Parallelism

**Proceedings, ... International Parallel Processing Symposium & ... Symposium on Parallel and Distributed Processing ,2000**

**Frontiers'96, the Sixth Symposium on the Frontiers of Massively Parallel Computation : October 27-31, 1996, Annapolis, Maryland : Proceedings** IEEE Computer Society,1996 Papers from the October 1996 symposium combine perspectives on architecture applications and systems with special focus on future systems concepts especially petaflops computing Includes sections on scheduling and routing applications and algorithms petaflops computing and point design studies SIMD I O techniques memory management synchronization networks and performance analysis Specific subjects include a quasi barrier technique to improve performance of an irregular application hardware controlled prefetching in directory based cache coherent systems and point designs for 100 TF computers using PIM technologies No index Annotation copyrighted by Book News Inc Portland OR

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