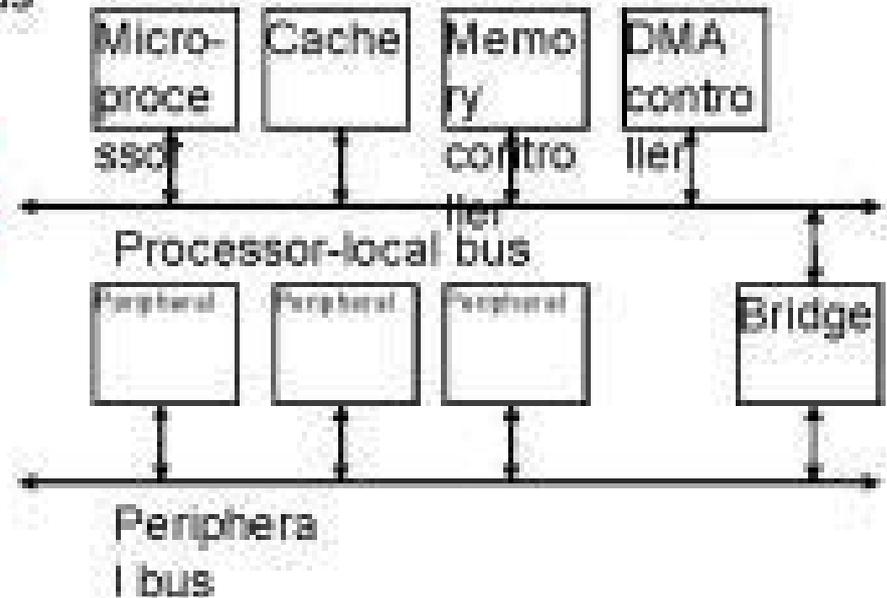


Multilevel bus architectures

- Don't want one bus for all communication
 - Peripherals would need high-speed, processor-specific bus interface
 - excess gates, power consumption, and cost; less portable
 - Too many peripherals slows down bus

- **Processor-local bus** 
 - High speed, wide, most frequent communication
 - Connected to cache, memory controllers, etc.



Advanced Multimicroprocessor Bus Architectures

M Carnoy



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