

ECE 4397: Digital Systems and Circuits

Assignment 1

Task 1: CMOS Inverter and Buffer

Objective: Design a CMOS inverter and a CMOS buffer. Provide a truth table for each circuit. The inverter should invert the input signal, and the buffer should output the same signal as the input. Use the following parameters: $V_{DD} = 5V$, $V_{SS} = 0V$, $k_n = 100 \mu A/V^2$, $k_p = 40 \mu A/V^2$, $V_{thn} = 1V$, $V_{thp} = -1V$.

The inverter should be designed to have a propagation delay of approximately 1 ns. The buffer should be designed to have a propagation delay of approximately 1 ns. The inverter should be designed to have a propagation delay of approximately 1 ns.

(a) Inverter Design

Design a CMOS inverter. Provide a truth table and a schematic diagram. The inverter should be designed to have a propagation delay of approximately 1 ns.

(b) Buffer Design and Characteristics

Design a CMOS buffer. Provide a truth table and a schematic diagram. The buffer should be designed to have a propagation delay of approximately 1 ns.

1. Provide a truth table for the inverter and buffer.
2. Provide a schematic diagram for the inverter and buffer.
3. Provide a truth table for the inverter and buffer.

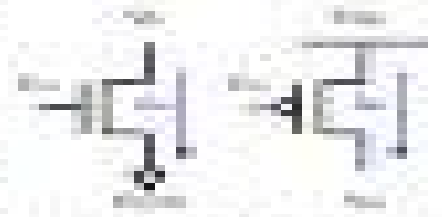


Figure 1: Schematic diagrams of a CMOS inverter and a CMOS buffer.

(c) Inverter Characteristics

Provide a truth table for the inverter. The inverter should be designed to have a propagation delay of approximately 1 ns.

1. Provide a truth table for the inverter.
2. Provide a schematic diagram for the inverter.
3. Provide a truth table for the inverter.

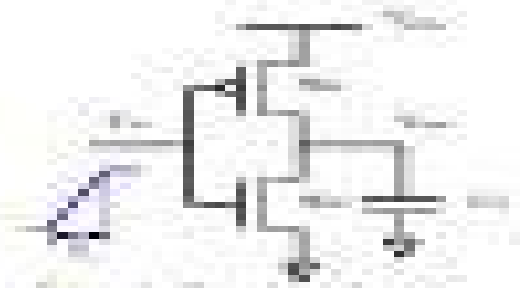


Figure 2: Schematic diagram of a CMOS inverter with a load capacitor.

Circuits Devices And Systems And Spice

Ralph Judson Smith



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Circuits Devices and Systems and Spice Book Ralph J. Smith,1996-07 **Circuits, Devices and Systems** Ralph J. Smith,Richard C. Dorf,1992-04-16 This book is also available through the Introductory Engineering Custom Publishing System If you are interested in creating a course pack that includes chapters from this book you can get further information by calling 212 850 6272 or sending email inquiries to engineerjwiley.com The authors offer a set of objectives at the beginning of each chapter plus a clear concise description of abstract concepts Focusing on preparing students to solve practical problems it includes numerous colorful illustrative examples Along with updated material on MOSFETS the CRO for use in lab work a thorough treatment of digital electronics and rapidly developing areas of electronics it contains an expansive glossary of new terms and ideas

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Midwest Symposium on Circuits and Systems ,1985

Schaum's Outline of Electronic Devices and Circuits, Second Edition Jimmie J. Cathey,2002-02-22 This updated version of its internationally popular predecessor provides and introductory problem solved text for understanding fundamental concepts of electronic devices their design and their circuitry Providing an interface with Pspice the most widely used program in electronics new key features include a new chapter presenting the basics of switched mode power supplies thirty one new examples and twenty three PS solved problems

Mechatronic Systems, Sensors, and Actuators Robert H. Bishop,2017-07-28 This book covers the key elements of physical systems modeling sensors and actuators signals and systems computers and logic systems and software and data acquisition It describes mathematical models of the mechanical

electrical and fluid subsystems that comprise many mechatronic systems **European Conference on Circuit Theory and Design, 5-8 September 1989** Institution of Electrical Engineers. Electronics Division,1989 **Proceedings of the ... IEEE International Caracas Conference on Devices, Circuits and Systems** ,2002 IEEE Circuits & Devices ,2006

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