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Designus Maximus Unleashed

Clive Maxfield



Designus Maximus Unleashed:

Designus Maximus Unleashed! Clive Maxfield, 1998-05-12 Designus Maximus Unleashed is more than a collection of article reprints in this book the original unedited text is revisited along with new insights and previously unpublished material all presented in the author's distinctive personal style The accompanying CD ROM includes a fully functioning virtual computer as well as BOOL Logic Synthesis MMLogic Multimedia Logic Design System and Analog Magic Clive Maxfield a popular columnist has collected his articles in a new order grouped by topic and expanded from the limits of magazine space These articles have been published in magazines such as EDN Electronic Design and Electronic Design Technology In addition he includes new material such as the history of computing logic design tools and the virtual computer Two chapters of personal perspective begin and end the text Clive Max Maxfield received his B SC in Control Engineering from Sheffield Polytechnic now Sheffield Hallam University England and began his career as a mainframe CPU designer He is currently a Member of the Technical Staff at Intergraph Computer Systems Huntsville AL In his spare time Max is a contributing editor to EDN magazine and a member of the advisory board to the Computer History Association of California In addition to numerous technical articles and papers Max is also the author of Bebop to the Boolean Boogie and the co author of Bebop BYTES Back An Unconventional Guide to Computers Based primarily on Designus Maximus series of articles from EDN magazine with new chapters and expanded text Includes a CD ROM including the Beboputer Virtual Computer Written by a popular columnist

Verilog Designer's Library Bob Zeidman, 1999-06-15 Ready to use building blocks for integrated circuit design Why start coding from scratch when you can work from this library of pre tested routines created by an HDL expert There are plenty of introductory texts to describe the basics of Verilog but Verilog Designer's Library is the only book that offers real reusable routines that you can put to work right away Verilog Designer's Library organizes Verilog routines according to functionality making it easy to locate the material you need Each function is described by a behavioral model to use for simulation followed by the RTL code you'll use to synthesize the gate level implementation Extensive test code is included for each function to assist you with your own verification efforts Coverage includes Essential Verilog coding techniques Basic building blocks of successful routines State machines and memories Practical debugging guidelines Although Verilog Designer's Library assumes a basic familiarity with Verilog structure and syntax it does not require a background in programming Beginners can work through the book in sequence to develop their skills while experienced Verilog users can go directly to the routines they need Hardware designers systems analysts VARs OEMs software developers and system integrators will find it an ideal sourcebook on all aspects of Verilog development

FPGAs: World Class Designs Clive Maxfield, 2009-02-24 All the design and development inspiration and direction a hardware engineer needs in one blockbuster book Clive Max Maxfield renowned author columnist and editor of PL DesignLine has selected the very best FPGA design material from the Newnes portfolio and has compiled it into this volume The result is

a book covering the gamut of FPGA design from design fundamentals to optimized layout techniques with a strong pragmatic emphasis In addition to specific design techniques and practices this book also discusses various approaches to solving FPGA design problems and how to successfully apply theory to actual design tasks The material has been selected for its timelessness as well as for its relevance to contemporary FPGA design issues ContentsChapter 1 Alternative FPGA ArchitecturesChapter 2 Design Techniques Rules and GuidelinesChapter 3 A VHDL Primer The EssentialsChapter 4 Modeling MemoriesChapter 5 Introduction to Synchronous State Machine Design and AnalysisChapter 6 Embedded ProcessorsChapter 7 Digital Signal ProcessingChapter 8 Basics of Embedded Audio ProcessingChapter 9 Basics of Embedded Video and Image ProcessingChapter 10 Programming Streaming FPGA Applications Using Block Diagrams In SimulinkChapter 11 Ladder and functional block programmingChapter 12 Timers Hand picked content selected by Clive Max Maxfield character luminary columnist and author Proven best design practices for FPGA development verification and low power Case histories and design examples get you off and running on your current project

Inside OrCAD Capture for Windows Chris Schroeder,1998-08-24 Inside OrCAD Capture for Windows is a reference manual and tutorial for engineers and technicians who use OrCAD as an engineering design assistance EDA tool This introduction to OrCAD is designed to give easy access to practical information Important subjects such as export of schematic data for use in circuit analysis or PCB design are expanded well beyond the information available in OrCAD s documentation The command reference is a complete listing and explanation of the OrCAD commands and functions A series of appendices provide important tips and techniques and information about linking OrCAD to other CAD CAE tools used in the electronics design process A utilities disk is included Exercises at the end of each chapter make this book appropriate for academic use The accompanying disk contains a parts library for the tutorial exercises and several useful utilities such as a bill of material sort making this book a valuable tool for the design engineer or engineering student Detailed tutorialOrCAD Capture for Windows is supported on Windows 3 1 95 and NT

Tips and techniques for design engineers

Real World FPGA Design with Verilog Ken Coffman,1999-12-08 The practical guide for every circuit designer creating FPGA designs with Verilog Walk through design step by step from coding through silicon Partitioning synthesis simulation test benches combinatorial and sequential designs and more Real World FPGA Design with Verilog guides you through every key challenge associated with designing FPGAs and ASICs using Verilog one of the world s leading hardware design languages You ll find irreverent yet rigorous coverage of what it really takes to translate HDL code into hardware and how to avoid the pitfalls that can occur along the way Ken Coffman presents no frills real world design techniques that can improve the stability and reliability of virtually any design Start by walking a typical Verilog design all the way through to silicon then review basic Verilog syntax design simulation and testing advanced simulation and more Coverage includes Essential digital design strategies recognizing the underlying analog building blocks used to create digital primitives implementing logic with LUTs clocking strategies logic minimization and more Key

engineering tradeoffs including operating speed vs latency Combinatorial and sequential designs Verilog test fixtures compiler directives and automated testing A detailed comparison of alternative architectures and software including a never before published FPGA technology selection checklist Real World FPGA Design with Verilog introduces libraries and reusable modules points out opportunities to reuse your own code and helps you decide when to purchase existing IP designs instead of building from scratch Essential rules for designing with ASIC conversion in mind are presented If you re involved with digital hardware design with Verilog Ken Coffman is a welcome voice of experience showing you the shortcuts helping you over the rough spots and helping you achieve competence faster than you ever expected Electronics Now ,1999-07

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