

Figure. 01

Basic Logic Gates with Truth Table

YES Gate



Truth Table:

INPUT		OUTPUT (Y)
A		
0		0
1		1

NOT Gate

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INPUT		OUTPUT (Y)
A		
0		1
1		0

AND Gate



INPUT		OUTPUT (Y)
A	B	
0	0	0
0	1	0
1	0	0
1	1	1

INPUT		OUTPUT (Y)
A	B	
0	0	1
0	1	1
1	0	1
1	1	0

NAND Gate



OR Gate



INPUT		OUTPUT (Y)
A	B	
0	0	0
0	1	1
1	0	1
1	1	1

NOR Gate



INPUT		OUTPUT (Y)
A	B	
0	0	1
0	1	0
1	0	0
1	1	0

XOR Gate



INPUT		OUTPUT (Y)
A	B	
0	0	0
0	1	1
1	0	1
1	1	0

INPUT		OUTPUT (Y)
A	B	
0	0	1
0	1	0
1	0	0
1	1	1

XNOR Gate



Basic Logic

Puja Mehta



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Table of Contents Basic Logic

1. Understanding the eBook Basic Logic
 - The Rise of Digital Reading Basic Logic
 - Advantages of eBooks Over Traditional Books
2. Identifying Basic Logic
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Basic Logic
 - User-Friendly Interface
4. Exploring eBook Recommendations from Basic Logic
 - Personalized Recommendations
 - Basic Logic User Reviews and Ratings

- Basic Logic and Bestseller Lists
- 5. Accessing Basic Logic Free and Paid eBooks
 - Basic Logic Public Domain eBooks
 - Basic Logic eBook Subscription Services
 - Basic Logic Budget-Friendly Options
- 6. Navigating Basic Logic eBook Formats
 - ePub, PDF, MOBI, and More
 - Basic Logic Compatibility with Devices
 - Basic Logic Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Basic Logic
 - Highlighting and Note-Taking Basic Logic
 - Interactive Elements Basic Logic
- 8. Staying Engaged with Basic Logic
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Basic Logic
- 9. Balancing eBooks and Physical Books Basic Logic
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Basic Logic
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Basic Logic
 - Setting Reading Goals Basic Logic
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Basic Logic
 - Fact-Checking eBook Content of Basic Logic
 - Distinguishing Credible Sources

13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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